

REFERENCE DESIGNATIONS
ARE ABBREVIATED.
PREFIX THE DESIGNATION
WITH UNIT NUMBER
OR ASSEMBLY DESIGNATION
OR BOTH. (MIL. STD. 168)

NOTES:

1. REV. LETTERS LISTED INDICATE STATUS OF EACH SHEET OF THIS MULTIPLE SHEET DRAWING.
2. CHECK INDIVIDUAL SHEETS FOR REVISION LETTER AGAINST THIS SHEET BEFORE USING THIS DRAWING.

REVISION INDEX

PAGE	CHG	PAGE	CHG	PAGE	CHG	PAGE	CHG	PAGE	CHG	PAGE	CHG	PAGE	CHG
1	A	8	A	15	A	22	A	29	A				
2	A	9	A	16	A	23	A	30	A				
3	A	10	A	17	A	24	A	31	A				
4	A	11	A	18	A	25	A	32	A				
5	A	12	A	19	A	26	A	33	A				
6	A	13	A	20	A	27	A	34	A				
7	A	14	A	21	A	28	A	35	A				

MODULE LIST

ITEM	DESCRIPTION	MODEL	QTY
1	SIGNAL AMPLIFIER	AH10	5
2	CABLE DRIVER	AX14	5
3	CABLE DRIVER CONTROLLED RISE & FALL	AX16	2
4	AND/OR BUFFER AMP.	BH10	10
5	D.C. FLIP FLOP	FH19	5
6	BASIC FLIP FLOP	FH20	13
7	BASIC FLIP FLOP	FH21	1
8	DIODE, GATE #1	GK51	19
9	AND/OR INVERTER	IH10	2
10	AND GATE/INVERTER	IH14	8
11	RELAY MODULE	KX12	1
12	ONE SHOT MULTIVIBRATOR	OX12	1
13	PRIMARY POWER DETECTOR	SK60	2

REPLACEMENT PARTS LIST

ITEM	DESCRIPTION	DESIGNATORS	QTY	SUPPLIER AND PART NO.	ZIP
1	CONNECTOR, SOLDER TAIL 47 CONTACT #7008-47	A, B, C, D 26 THRU 45	80	ELCO CORP. 7008-047-000-502	19090
2	DIODE, SILICON SWITCHING	CR1, CR2 P160 CR1 THRU CR3 P159 CR4 THRU CR11 P157 CR1 THRU CR20 P161 CR1 THRU CR27 P155	2 3 11 20 27	CONT. DEVICES IN914A OR IN3064 FAIRCHILD IN914A OR IN3064 GENERAL ELECTRIC IN914A OR IN3064 HUGHES IN914A OR IN3064 ITT SEMICONDUCTOR IN914A OR IN3064 TEXAS INST. IN914A OR IN3064	90250 94041 42302 92663 01841 75222
3	RESISTOR, 1/2W. ± 2% FIXED FILM	R1 THRU R40 P159 R1 THRU R28 P157 R29 P157 R30 THRU R32 P157 R1 THRU R20 P161 R4 THRU R30 P155	40 28 1 3 20 27	AMPEREX E009 SERIES CORNING C-20 INT. RES. CO. MDA, MEA TEXAS INST. GP 1/2 WELWYN F-20	11802 16701 52602 75222 44091
4	RESISTOR, 1/2W. ± 2% FIXED FILM	R41 THRU R43 P159 R33 THRU R43 P157 R20 THRU R39 P161 R4 THRU R30 P155	3 11 20 27	AMPEREX E009 SERIES CORNING C-20 INT. RES. CO. MDA, MEA TEXAS INST. GP 1/2 WELWYN F-20	11802 16701 52602 75222 44091
5	INDUCTOR, MOLDED .75 TO 10000 MICROHENRY	L1 THRU L40 P159 L1 THRU L28 P157 L29 P157 L30 THRU L32 P157 L1 THRU L20 P161 L1 THRU L3 P-155	40 28 1 3 20 3	BELEVAN ELECT. 1537 SERIES 085 J.W. MILLER CO. 9310-SERIES 085 MYTRONICS INC. DD DR WEE 035 STANWYCK WINDING 20,000 SERIES 085	14052 90003 07922 12551

PAGE 1

DRAWING NO.		9367	
DRAWING NO.		131564	
REVISION		7	
DESCRIPTION		DIAGRAM, LOGIC, CONTROLLER	
PART NO.		D 131820	
SHEET		5	

REV	DESCRIPTION	DATE
A	RELEASED TO MFG.	12/18/60

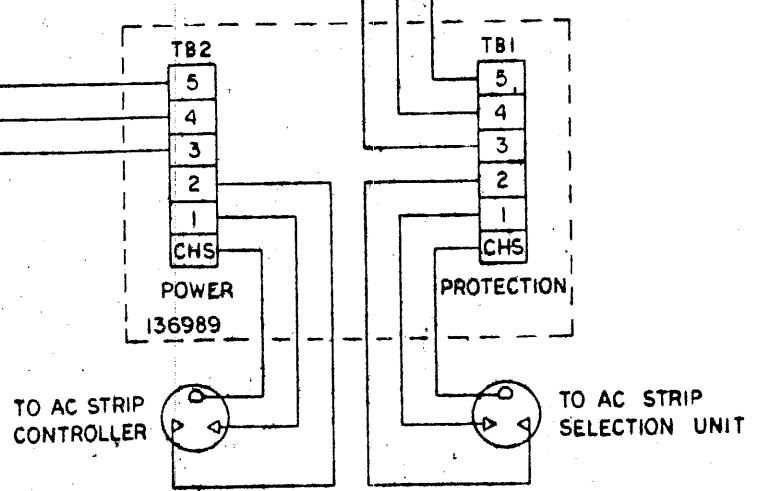
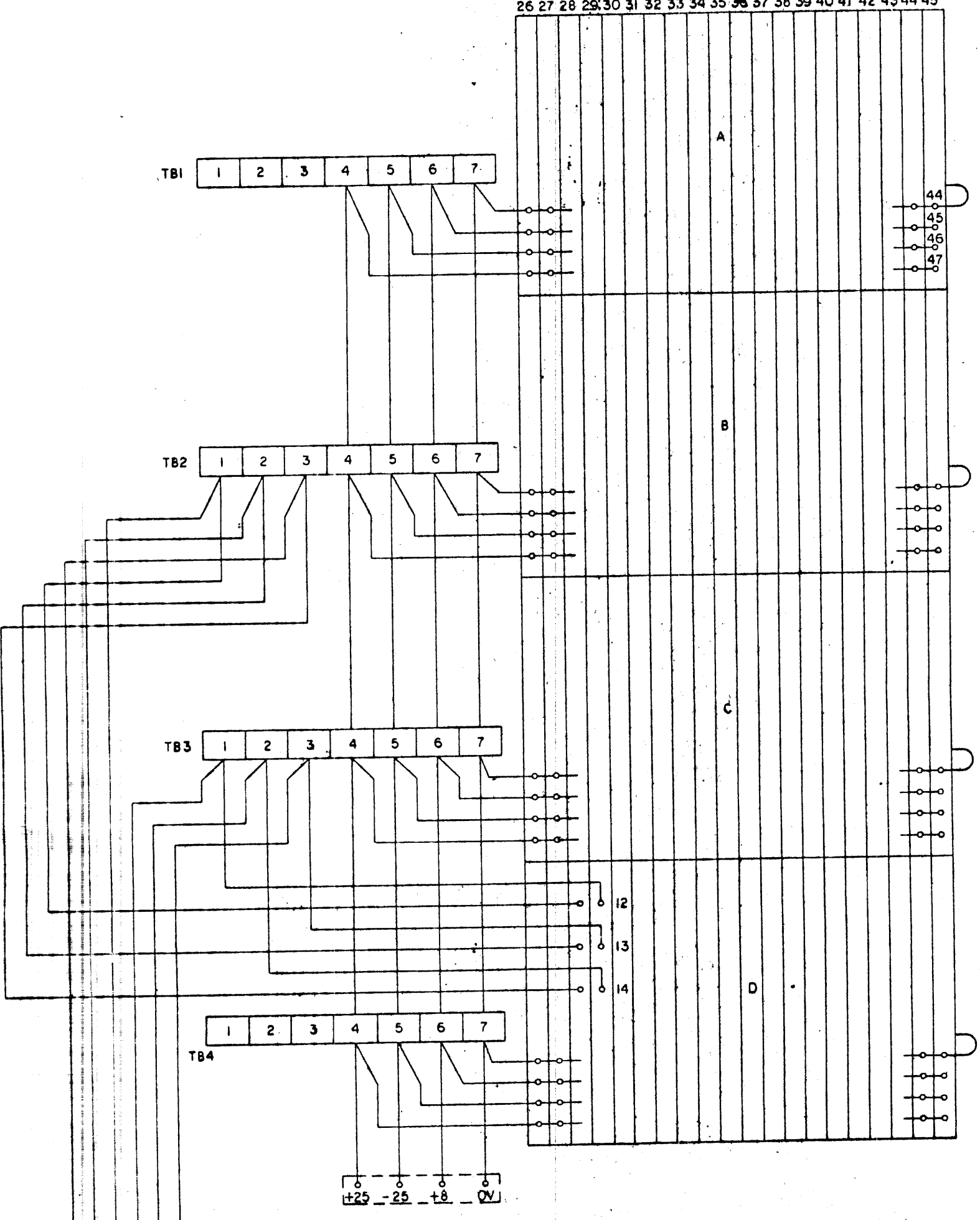
DIRECTORY

A		K		S	
ACT	32	KQ1 — KQ4	19	SAC	9
AFA	6	KO5 — K07	20	SHS	28
AF3	6			S10	31
AFC	6			S1P	9
AFL	6	L		SKE	31
AIN	6	LDA	6	SKP	31
BA11 — BA17	5	LDS	28	SKR	31
OA09 — OA11	6	LDV	28	SPR	24
OA12 — OA17	7	LQZ	28	STO	5
OA18 — OA23	8	LRC	28	STV	28
		LR1 — LR4	30	S01 — S06	22
		LSC	18	S07 — S12	23
		LVR	28		
B				U	
BSC	9	M		US1 — US4	5
BUC	32	MSA	6	U01, U02	11
		MO1 — MO2	18	U03	12
				U04, U05, U06	13
		N		UIU	11
C				23U	11
CD1 — C06	9	NNL	19		
CLH	9	NUF	13	V	
CLK	17	NXL	19	V01 — V12	27
CNT	18			9V01 — 9V12	33
CLRI	9				
CO9 — C023	5	P		W	
C12W — C023W	32	PCP	25	WD1, — WD4	29
		PIN	34	WEN	29
		PNC	12	WES	31
D		PST	14	WMS	31
DAP	34	PTQ	5	WLC	10
DMA	32	PT1	5	WPC	18
DHW	32	PT2	5	W50, W60	32
DRA	6	PUF	31	W90	32
DRK	18	PWR	17	W10 — W14	32
DRV	28	PO1 — PO4	24		
DRZ	28	PO5 — PO6	25	X	
DO1 — D06	34			X01 — X04	14
		R		X05	13
		RCN	30	X06, X07	15
E		REN	30	OXB, X09	16
ECW	31	RF1	13	X12	33
EDS	32	RL1, RL2	17		
ERP	14	RT1	34	Z	
ERW	10	RT0	5	Z01 — Z12	26
EVP	25				
E01	21			0	
				O0M	18
				O2M	18
F					
F01, F02	10				
OOF	10				
O1F	10				
O2F	10				
G					
GS1 — GS4	34				
GO1, GO2	34				
H					
HSD	6				
I					
IDN	32				
IDT	32				
INT	5				
IOC	32				
IWO	32				

SDS SCIENTIFIC DATA SYSTEMS SANTA MONICA, CALIFORNIA		TITLE DIAGRAM, LOGIC, CONTROLLER		REVISIONS NO. DESCRIPTION DATE APPROVED A MFG RELEASE	
DATE 11-2-64 0-7-64	DRAWING NO. D	QTY. 131820	SHEETS 2 OF 2	DATE 131820	APPROVED A

RAD

26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45

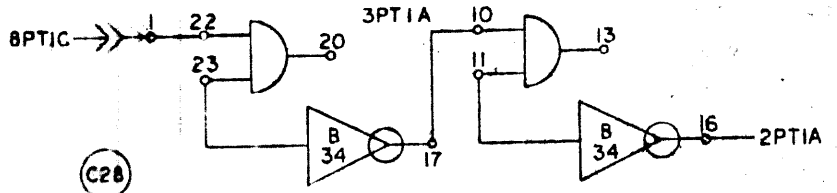


PAGE 3

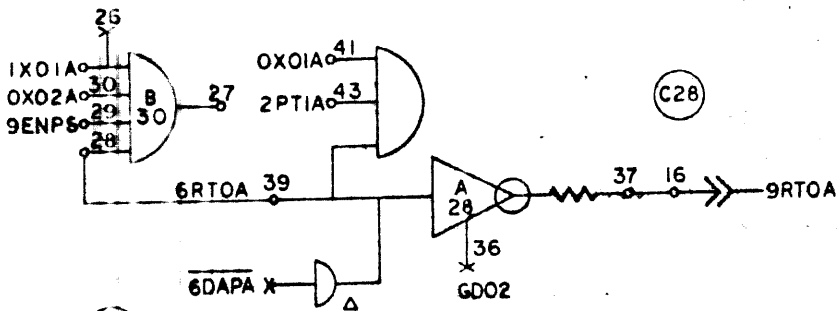
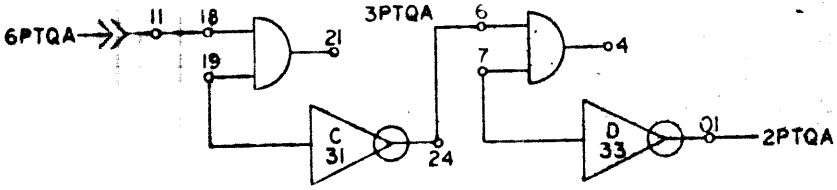
505 SCIENTIFIC DATA SYSTEMS
 SANTA MONICA, CALIFORNIA
 TITLE: **DIAGRAM, LOGIC, CONTROLLER**
 PART NO: **D 131820**
 SCALE: **A**
 SHEET: **3 OF 35**

REV.	DESCRIPTION	DATE	APPROVED
A	MFG RELEASE	1/18/60	[Signature]

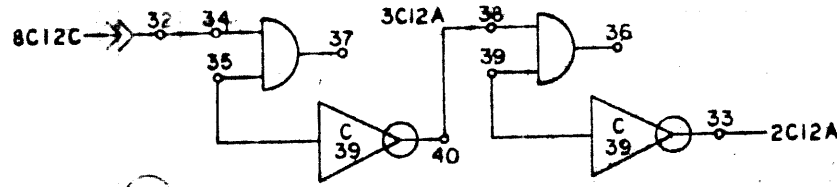
POT, UNIT SELECT, X-Y SELECT, INTERRUPT



(C28)

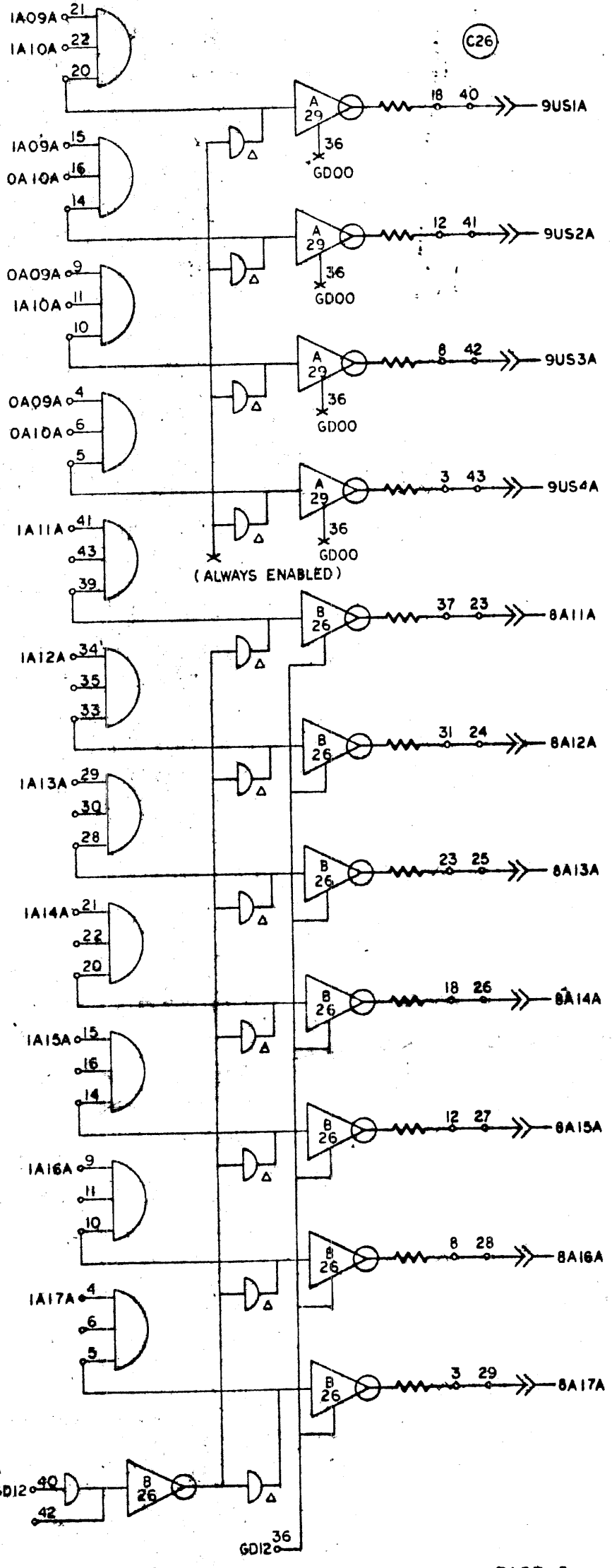
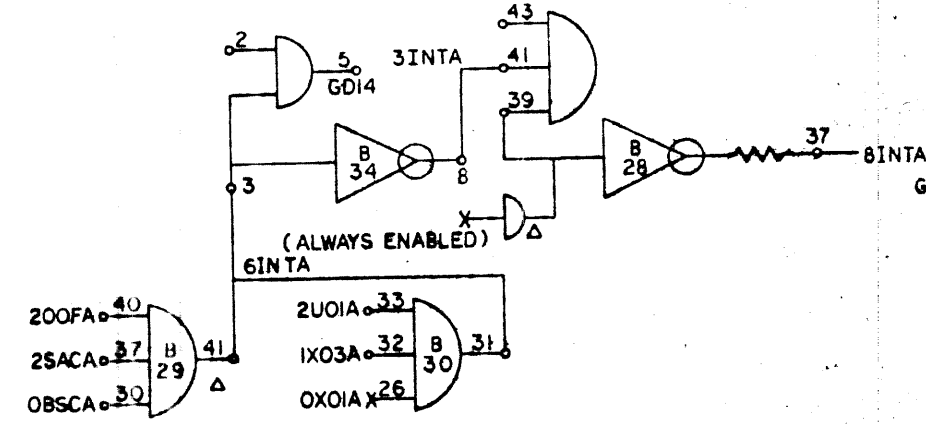
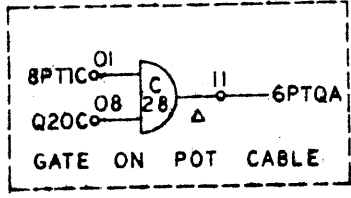


(C28)



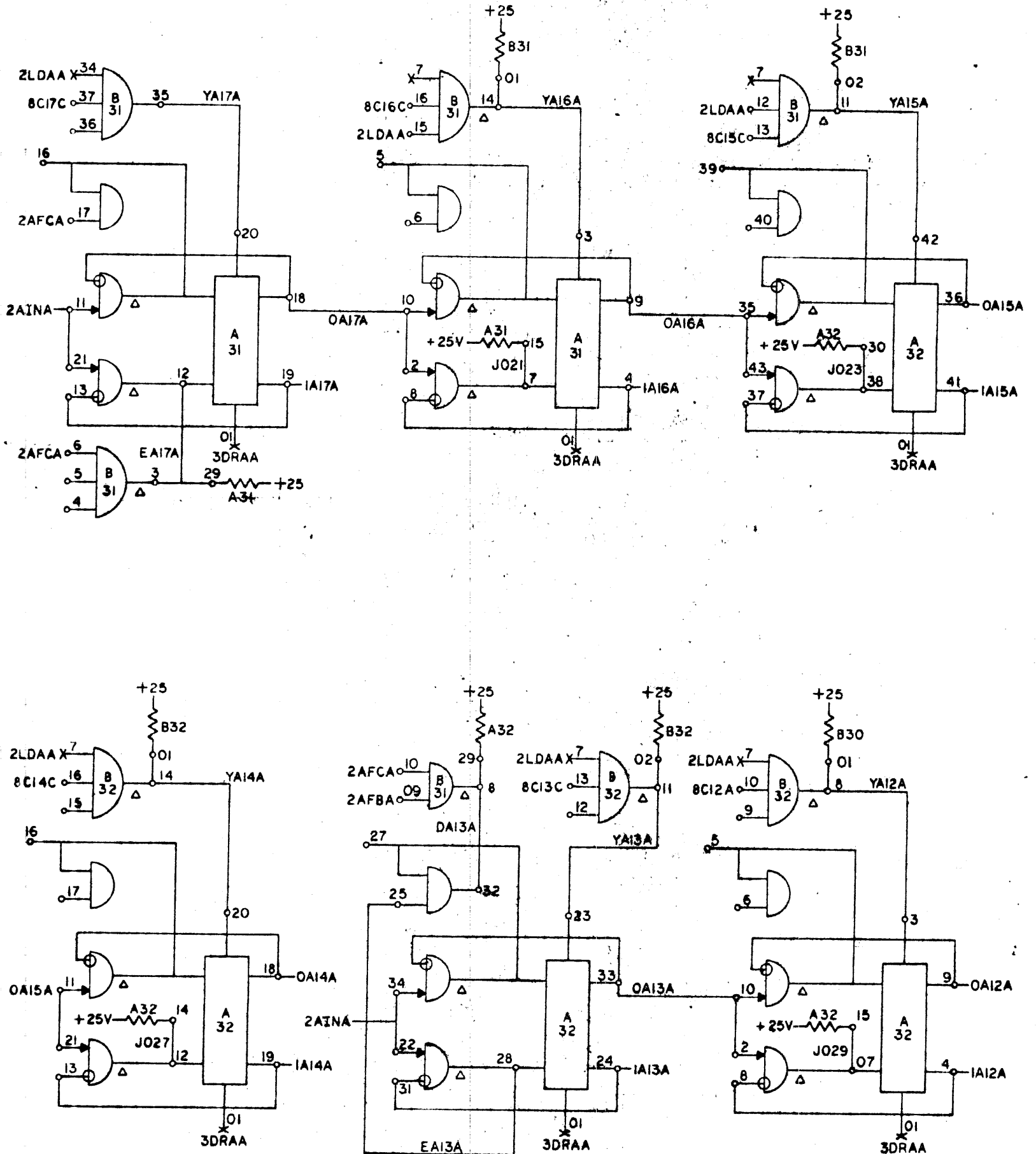
(C28)

- 2 8PT2C
- 17 8STOC
- 29 8C09C
- 30 8C10C
- 31 8C11C
- 33 8C13C
- 34 8C14C
- 35 8C15C
- 36 8C16C
- 37 8C17C
- 38 8C18C
- 39 8C19C
- 40 8C20C
- 41 8C21C
- 42 8C22C
- 43 8C23C



SCALE: D 151820 SHEET 5 OF 35	DIAGRAM, LOGIC, CONTROLLER	REVISIONS	
		REV. DESCRIPTION	DATE APPROVED
MFG. RELEASE		151820	1

ADDRESS REGISTER
A12 → A17

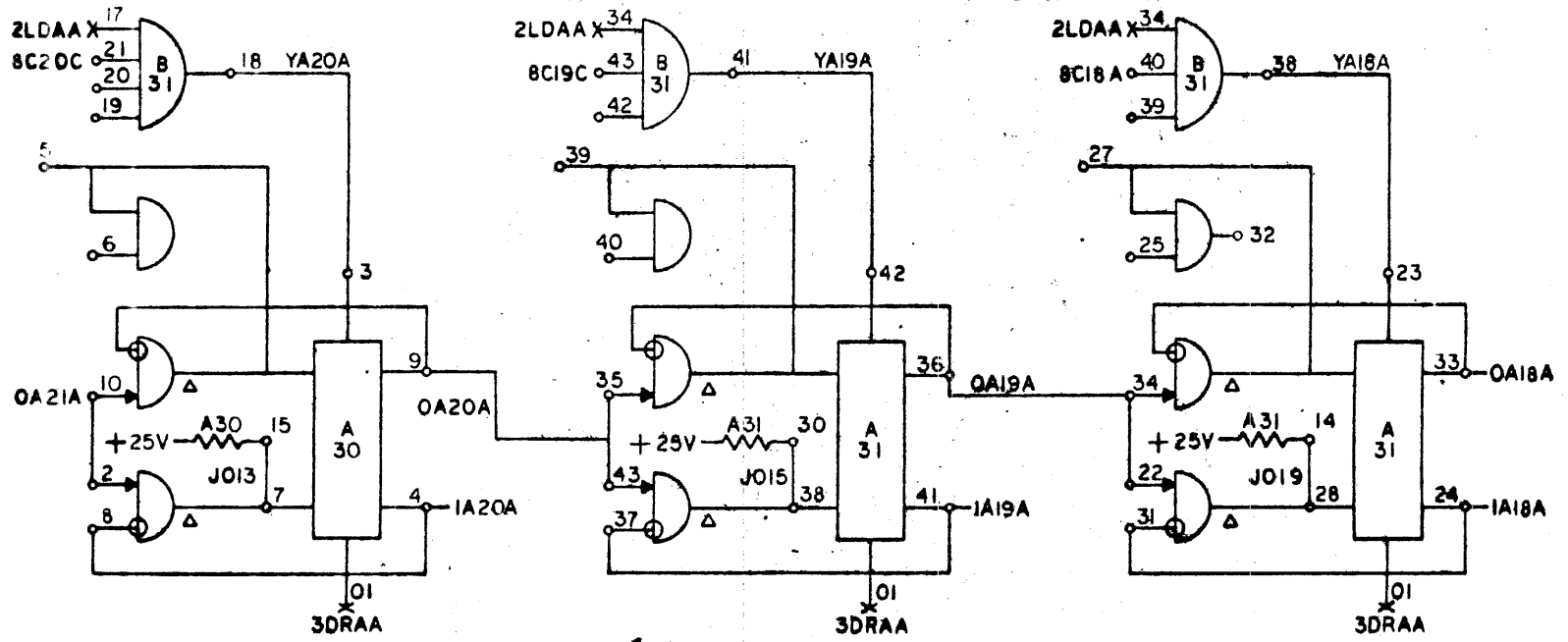
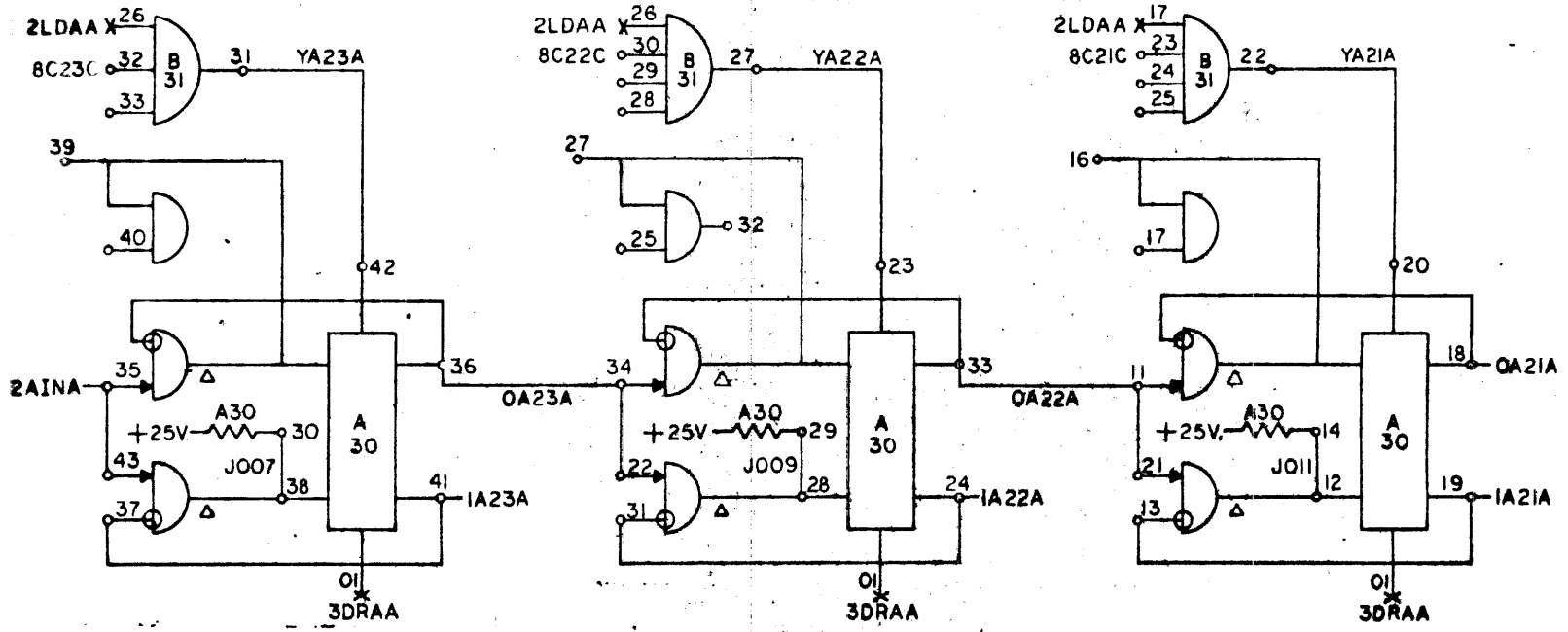


3777
1001

SCALE	DATE	REV	DESCRIPTION
1/8"	5/54	D	131820
DO NOT SCALE DRAWING			
DIAGRAM, LOGIC, CONTROLLER			
S.D.S.			
7 OF 35			

REV	DESCRIPTION	DATE	APPROVED
A	VFG RELEASE		
3 5 20			

ADDRESS REGISTER
A18 → A23



1600 / 000001
12 13 14 15 16 17 18 19 20 21 22 23

SCALE: 1/8" = 1"

DIAGRAM LOGIC,
CONTROLLER

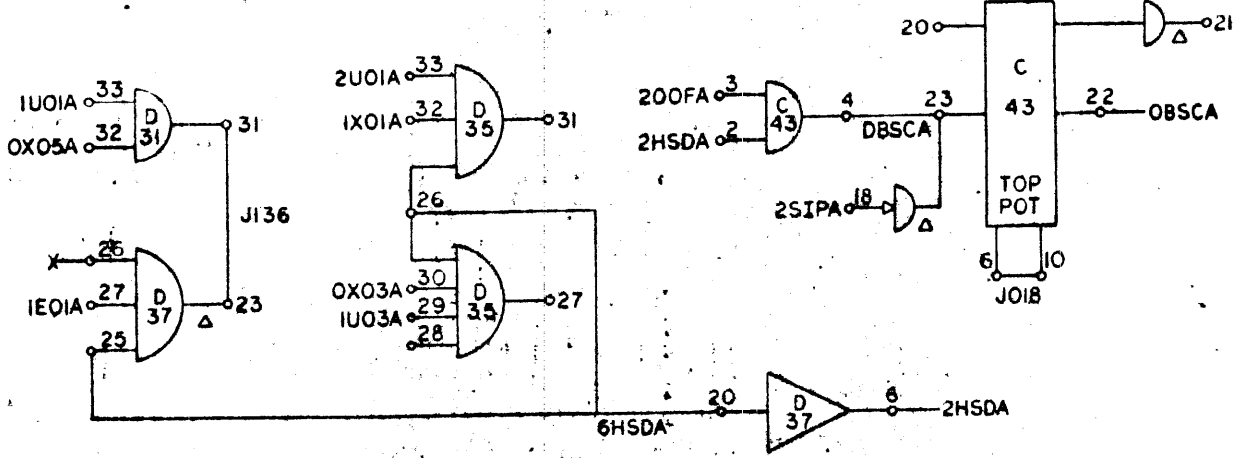
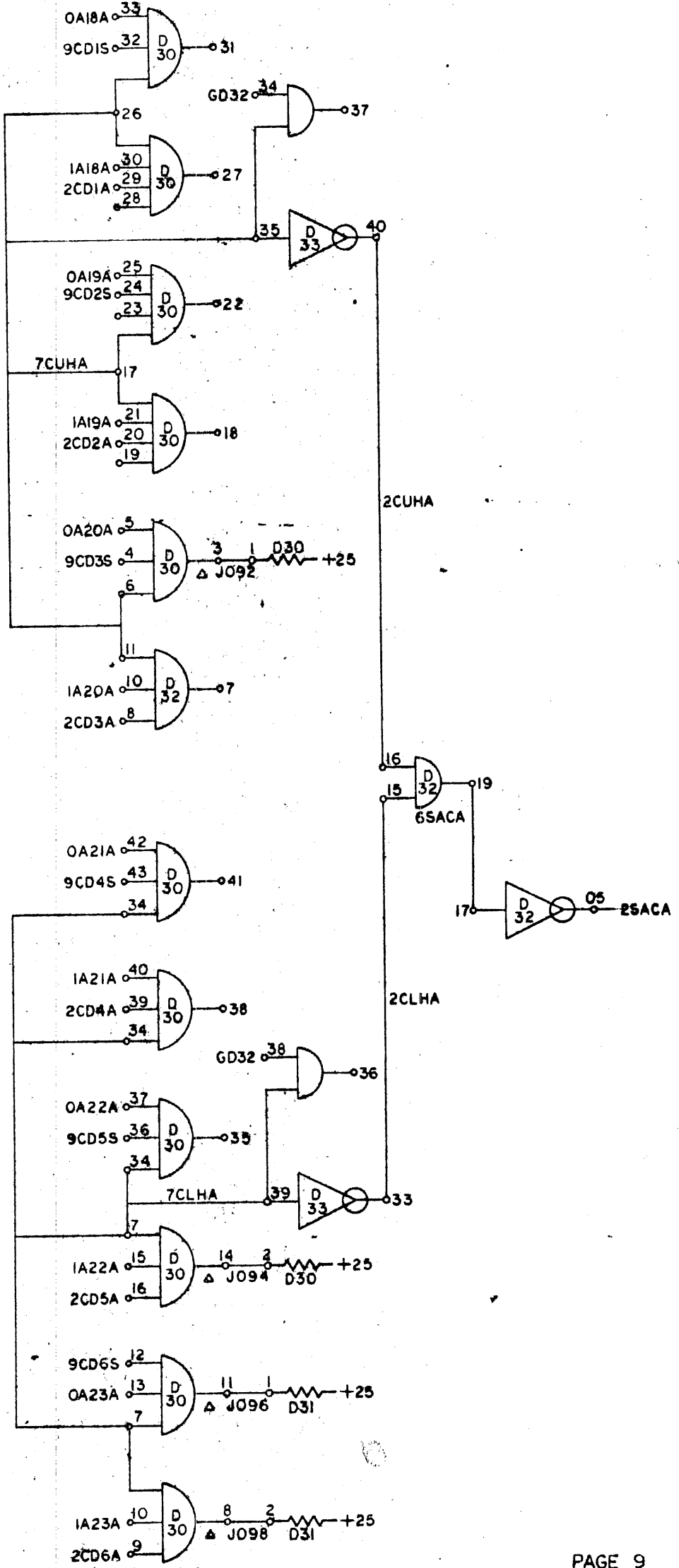
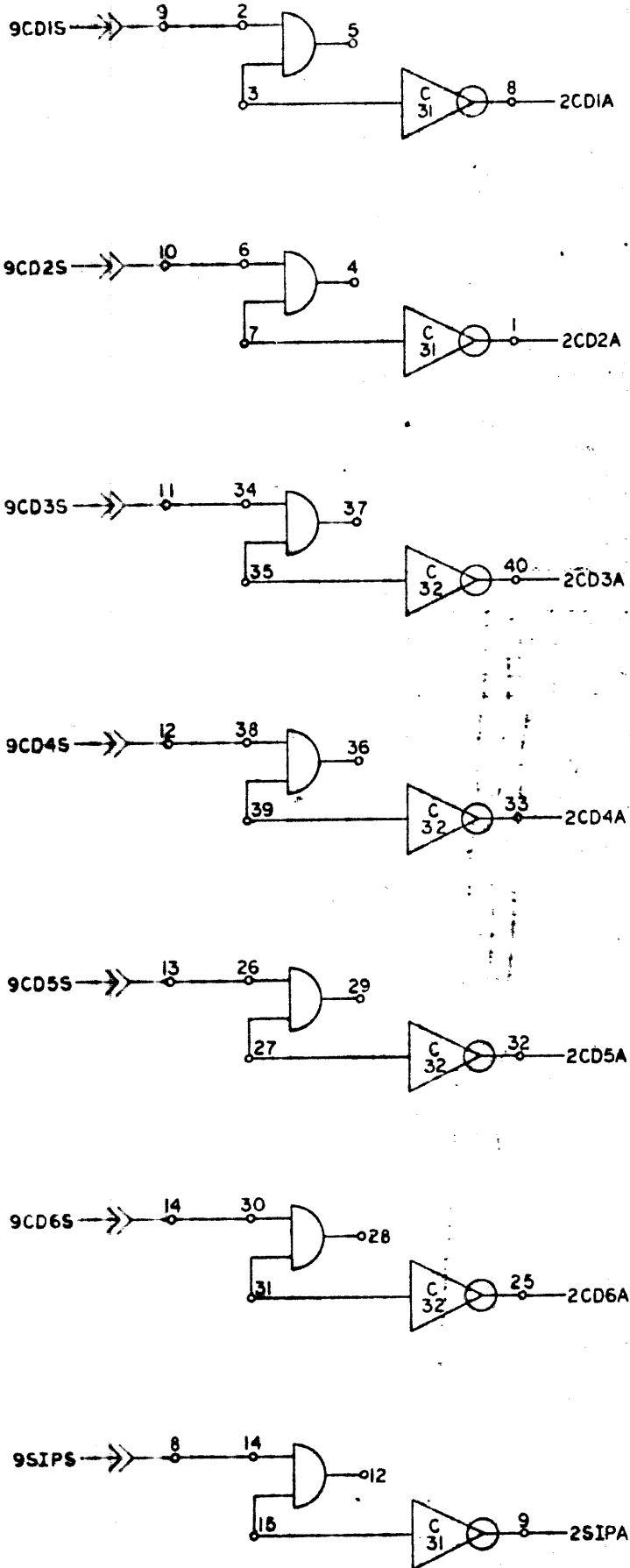
131820

8 OF 25

REV	DESCRIPTION	DATE	BY
A	MFG RELEASE		

SAC, BSC, HSD,
CDI THRU CD6, SIP

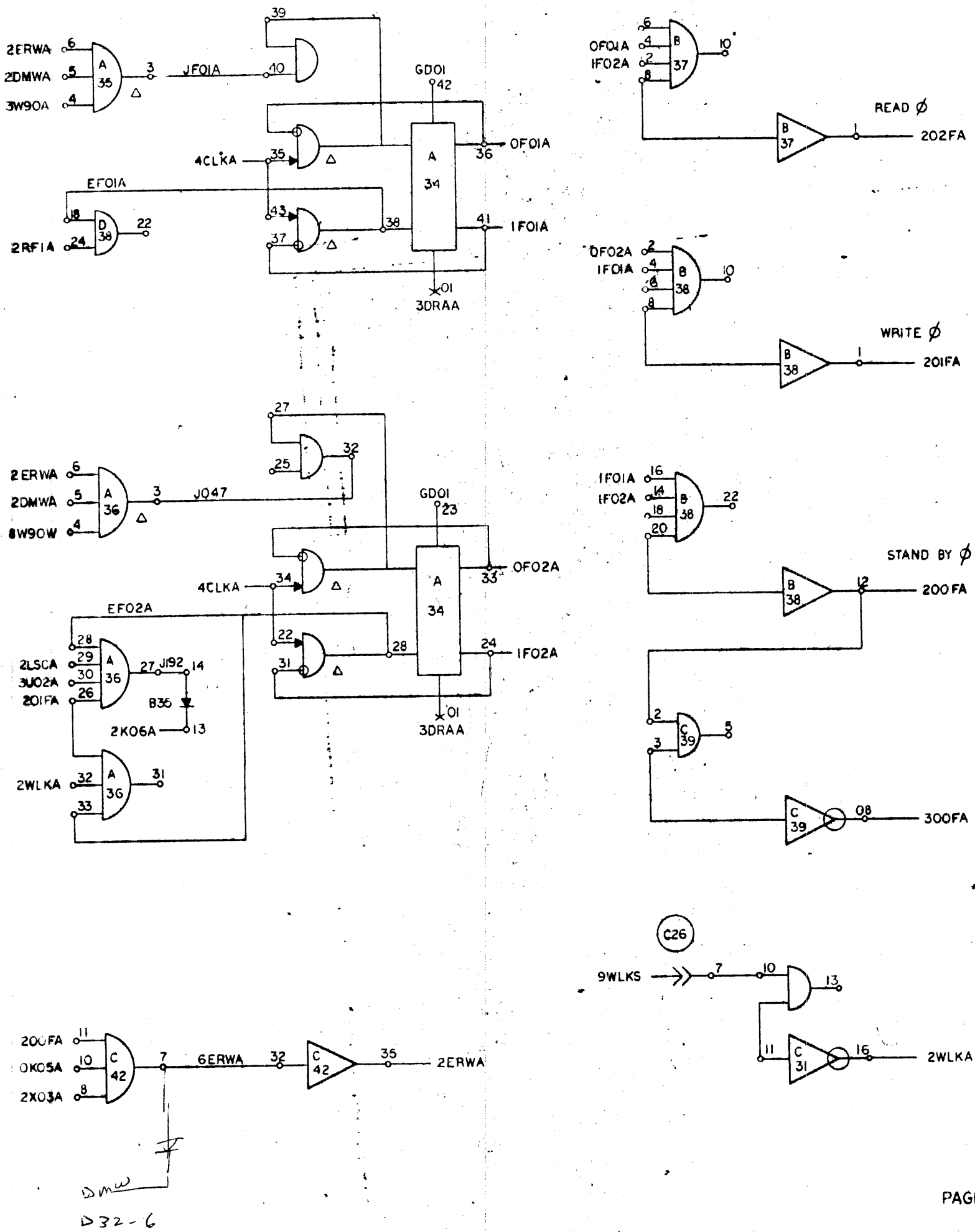
C.26



TITLE DIAGRAM, LOGIC, CONTROLLER	
PART NO. D 31820	REV. NO. A
SHEET 9 OF 35	

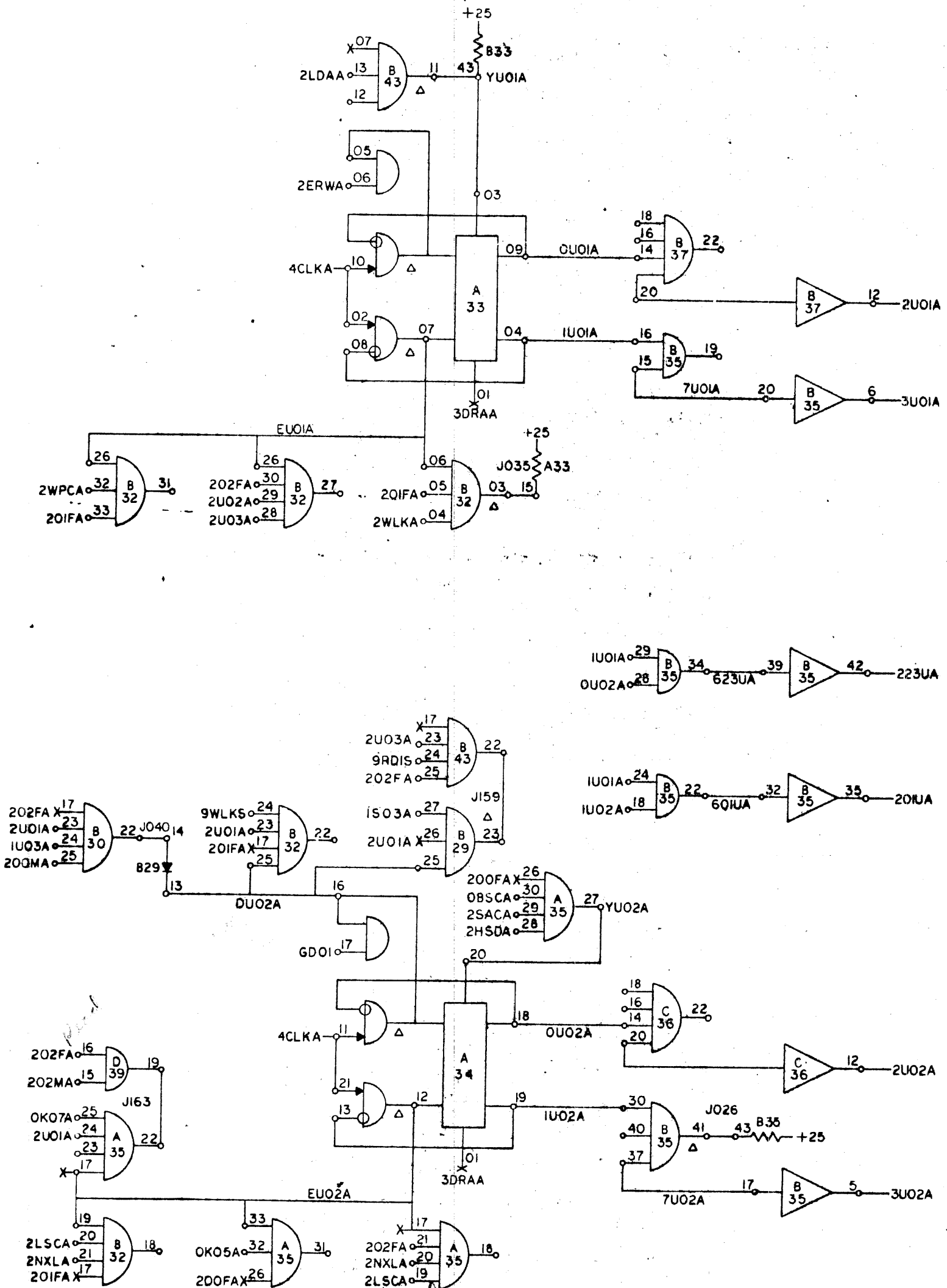
REV.	DESCRIPTION	DATE	APPROVED
A	MFG RELEASE		
REVISIONS 3 320		3	

PHASE REGISTER
FO1, FO2, OOF, OIF, O2F
ERW, WLK



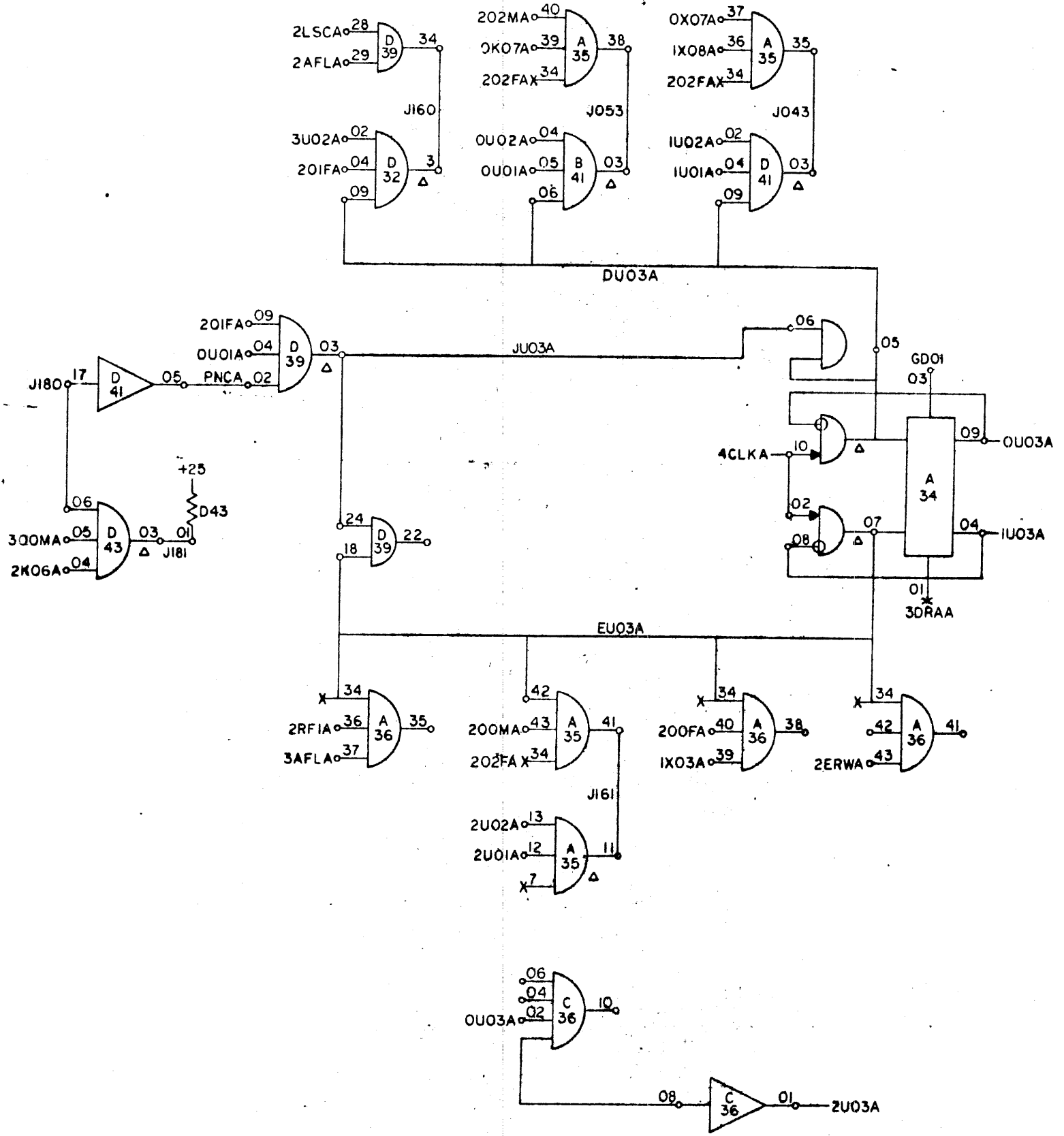
		DIAGRAM, LOGIC, CONTROLLER		131820		1007-55	
REV	DESCRIPTION	CHK	DATE	APPROVED	DATE	APPROVED	DATE
A	4fg RELEASE						
REVISIONS				11920 A			

U01, U02, 223U, 201U



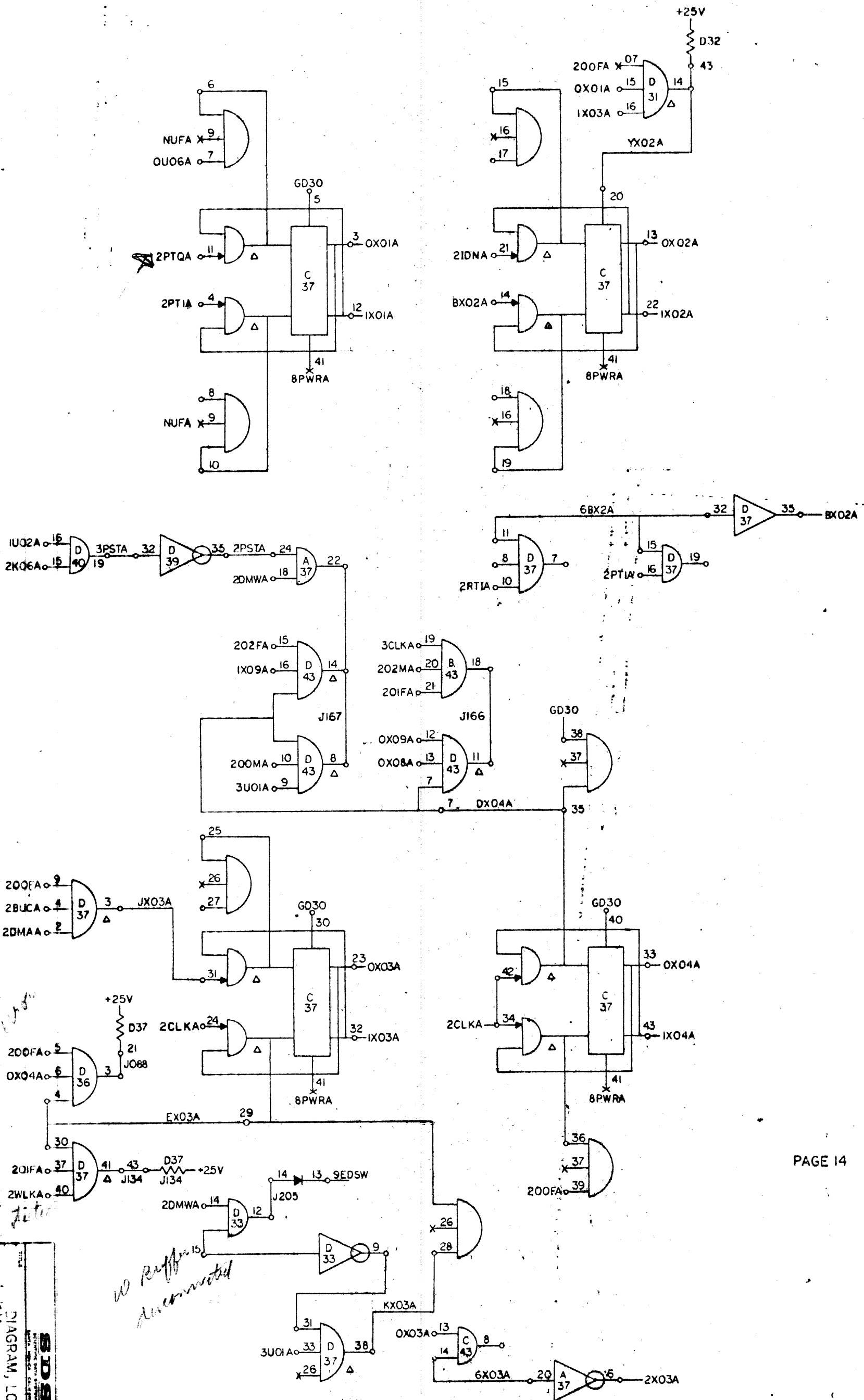
TITLE DIAGRAM, LOGIC, CONTROLLER		REVISIONS 1 31320	
DRAWING NO. 131820		DATE 11/66	
SCALE D		MFG RELEASE 11/66	
SHEET 11 OF 35		APPROVED [Signature]	

U03



<p>DATE: 12-25-65</p>		<p>REV: A</p>	
<p>DESCRIPTION: MFG RELEASE</p>		<p>REV: 320</p>	
<p>DATE: 131820</p>		<p>APPROVED: MN</p>	
<p>DIAGRAM, LOGIC, CONTROLLER</p>			
<p>31820</p>			

X01 THRU X04



3-15-66
 V. Sklar
D
 131820
 A

SDS
 SYSTEMS DEVELOPMENT CORPORATION
 DIAGRAM, LOGIC,
 CONTROLLER

14 J 65

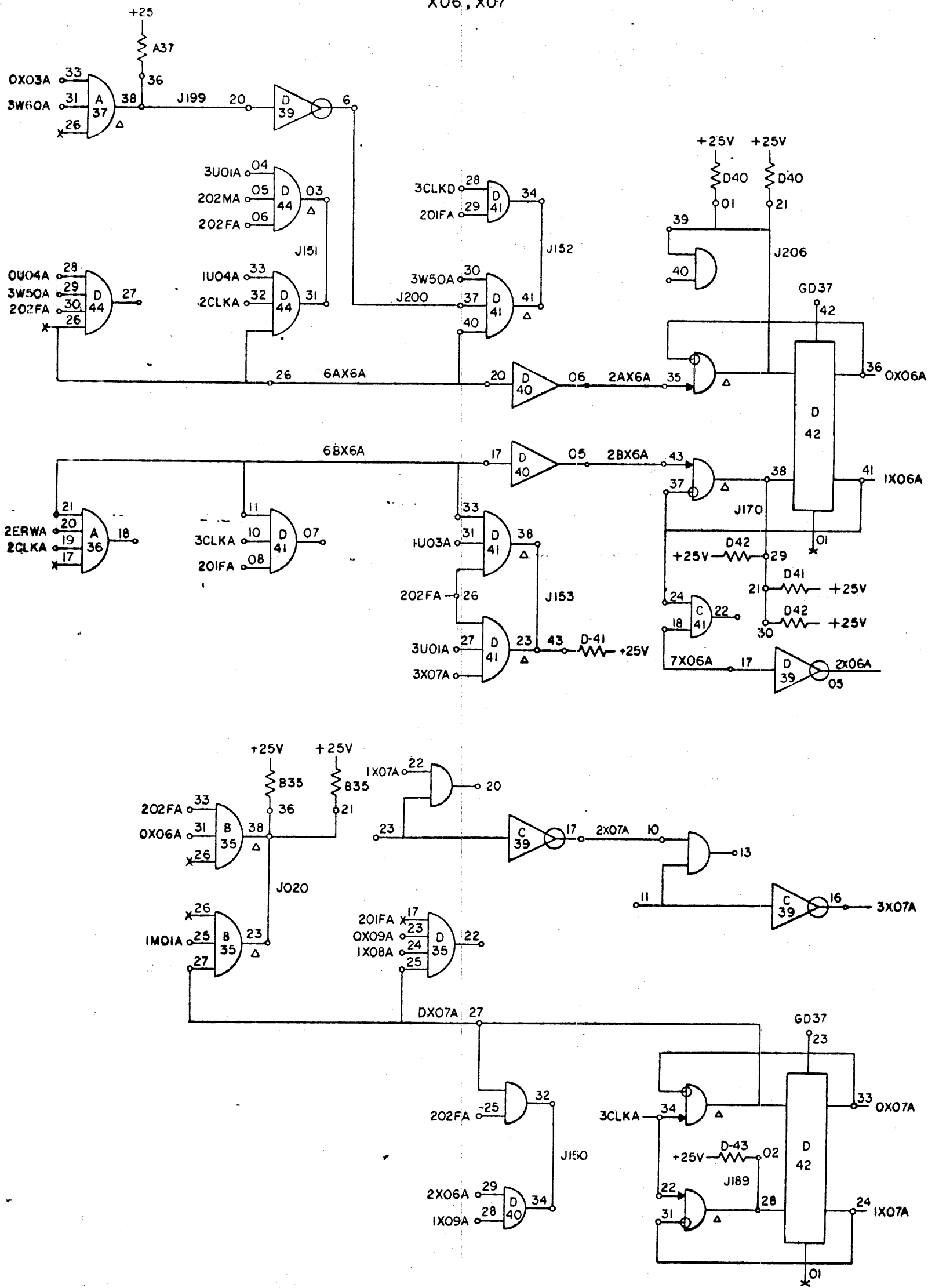
REVISIONS		DATE	APPROVED
1	VFG RELEASE	3 30 66	A

*no buffer
 disconnected*

Rate 1/30

Write title

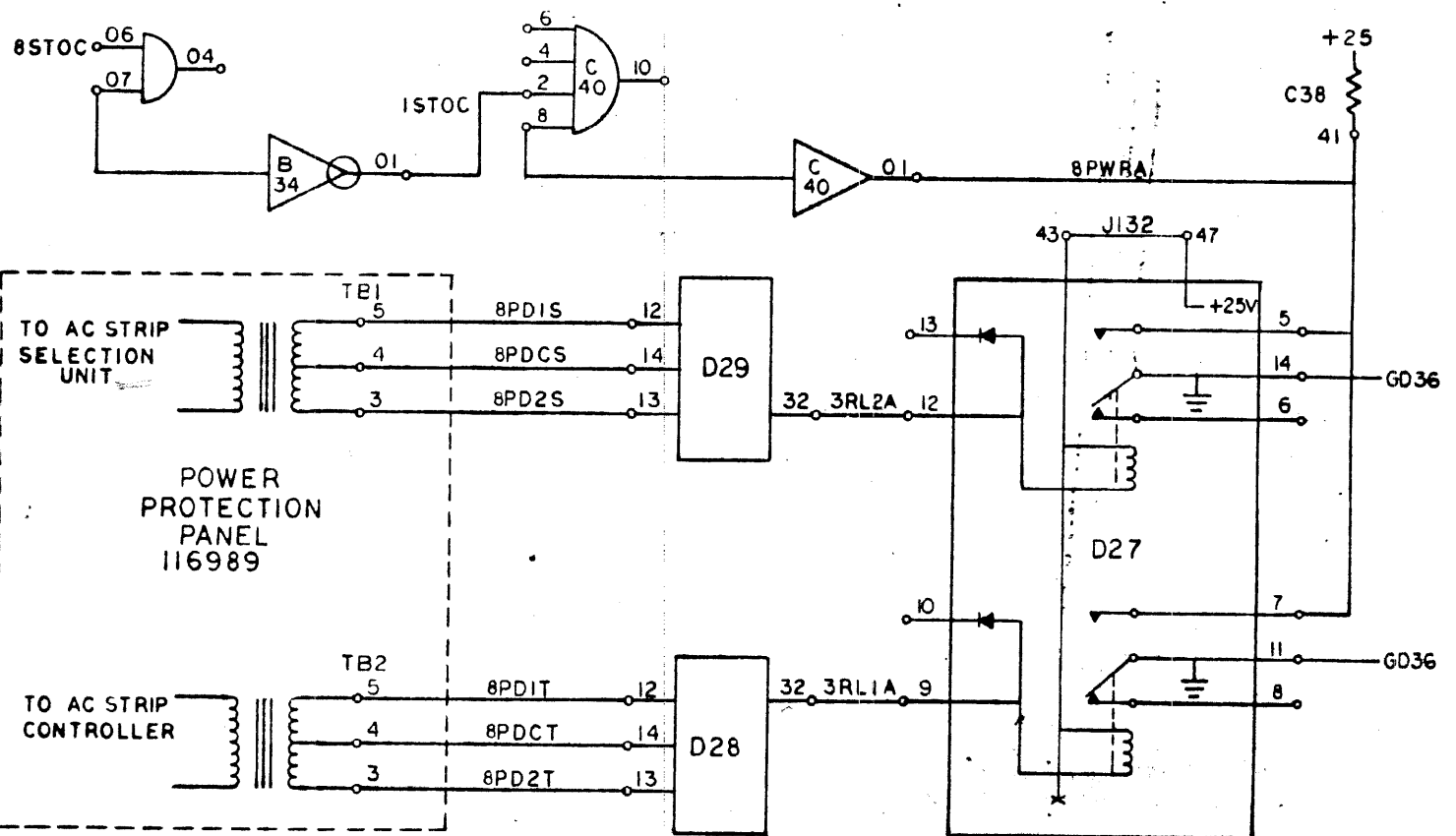
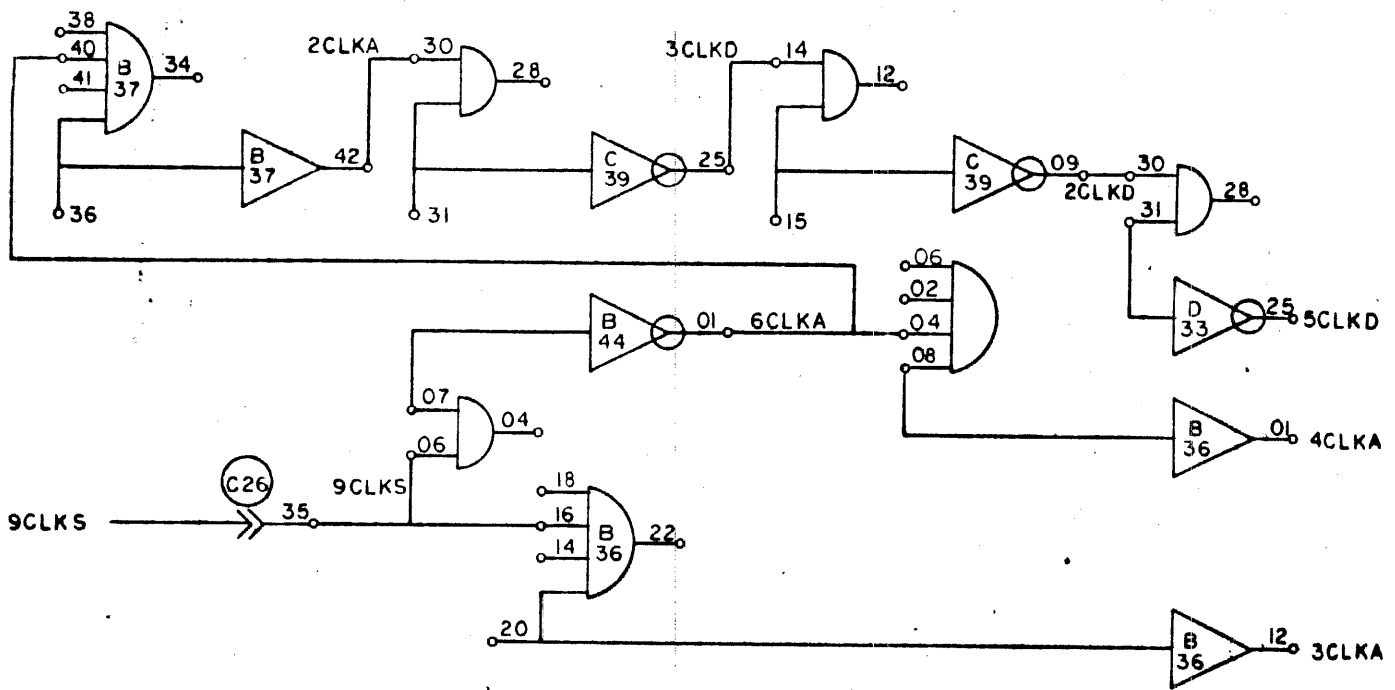
X06, X07



TITLE: **DIAGRAM, LOGIC, CONTROLLER**
 PART NUMBER: **131820**
 SCALE: **150:1**
 DATE: **150-35**

REVISIONS	
REV	DESCRIPTION
1	MFG RELEASE
2	
3	
4	
5	

CLOCK, POWER FAIL

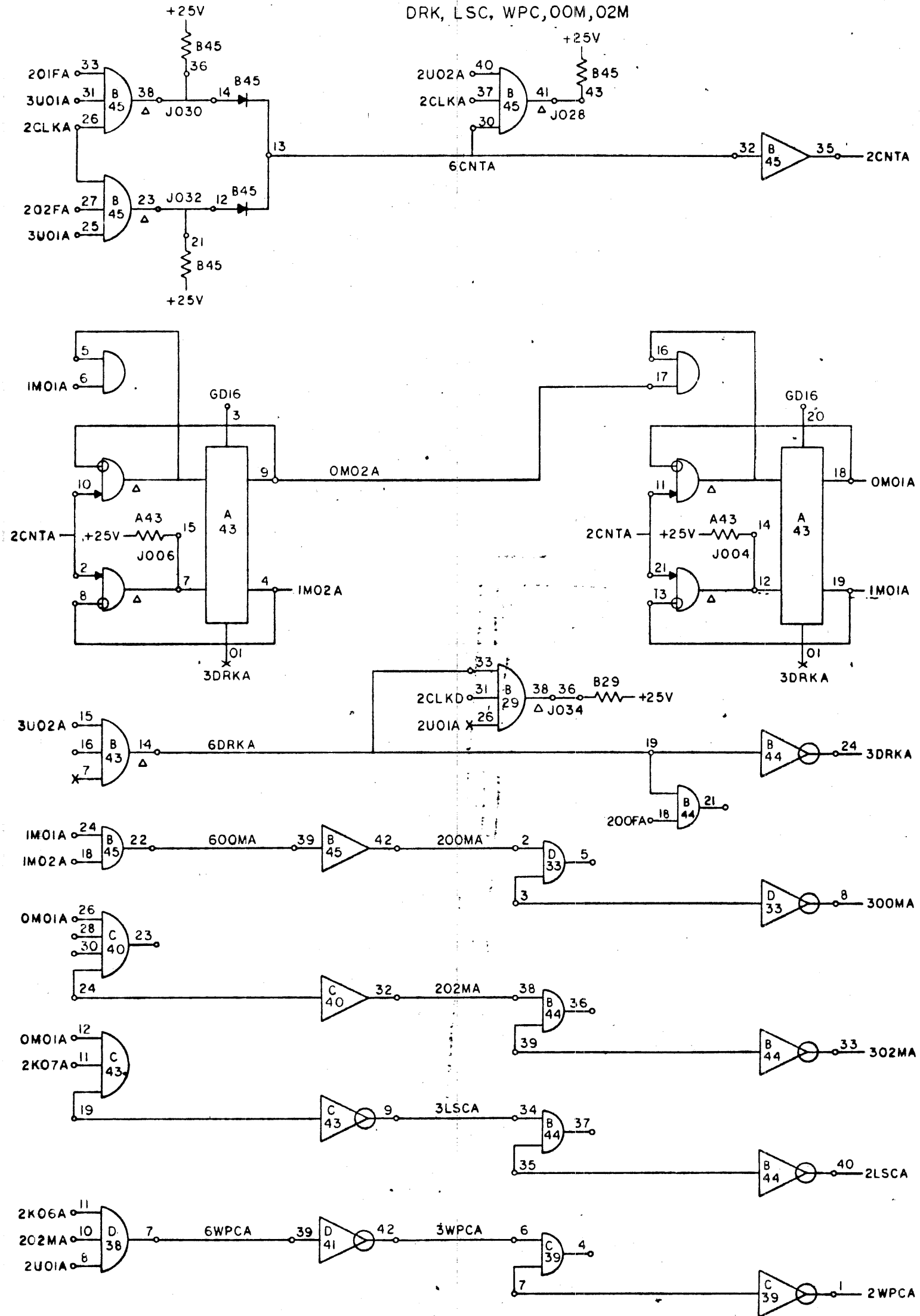


DIAGRAM, LOGIC,
CONTROLLER

131820

REV	DESCRIPTION	CHK	DATE	APPROVED
A	RELEASE			
			131820	A

MODULO THREE COUNT
MOI-MO2
DRK, LSC, WPC, OOM, O2M

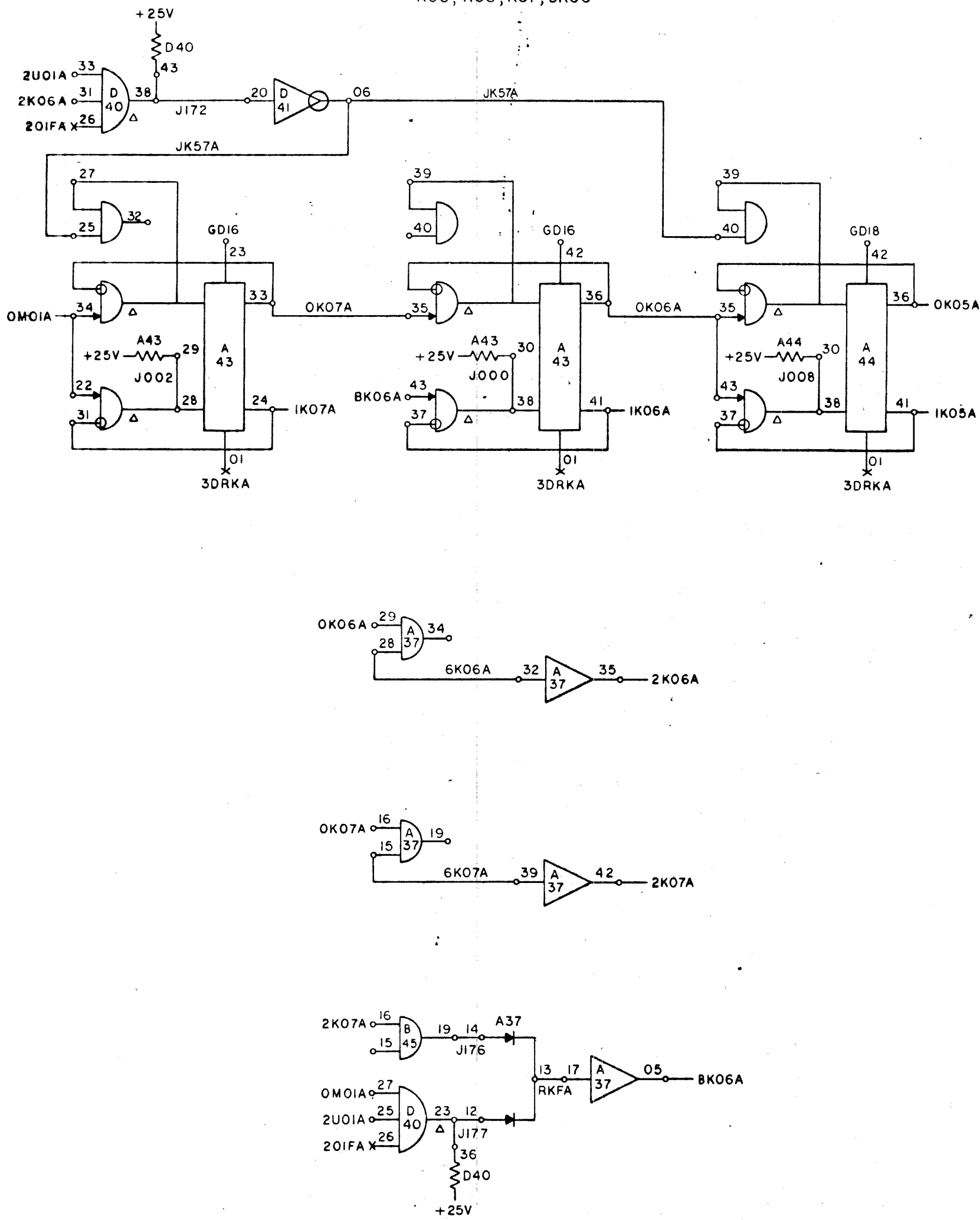


REV	DESCRIPTION	DATE	BY
A	MFG RELEASE	3/20/56	...

REV	DESCRIPTION	DATE	BY
D	131820	8-16-56	...

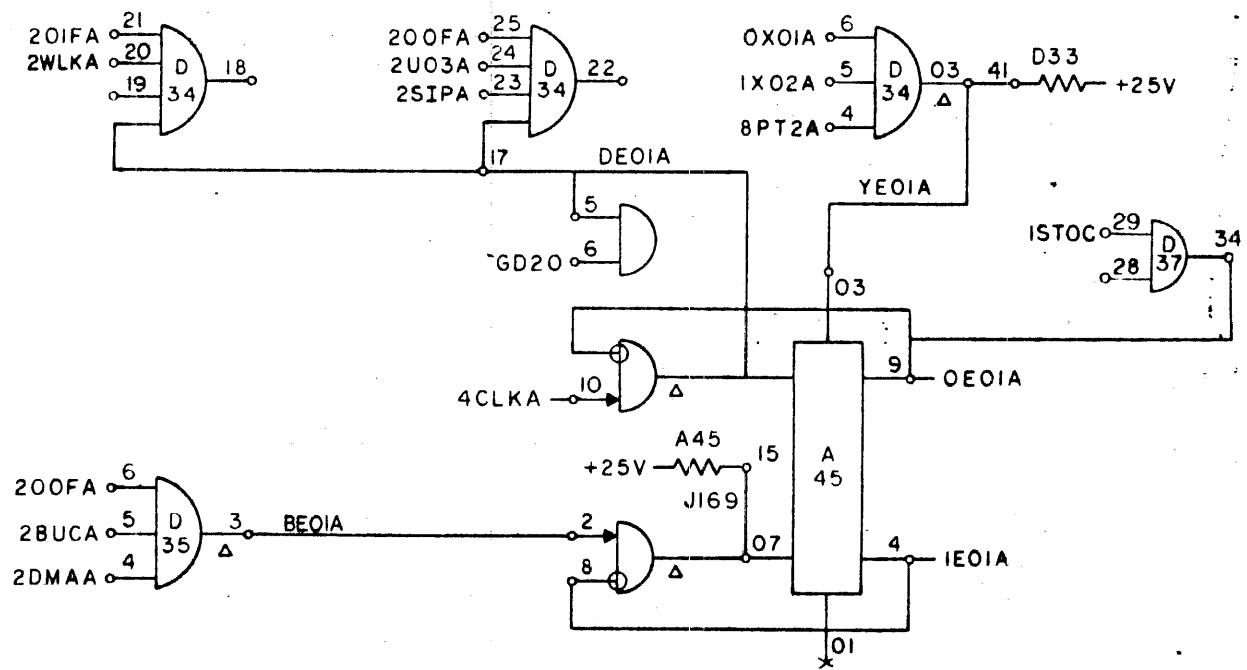
<p>DIAGRAM, LOGIC, CONTROLLER</p>			
<p>320 A</p>			

CHARACTER COUNT REGISTER
K05, K06, K07, BK06



<p>SDS SYSTEMS DEVELOPMENT CORPORATION SAN JOSE, CALIF. 95128</p>		<p>DIAGRAM, LOGIC, CONTROLLER</p>		<p>131820</p>		<p>5-10-66</p>		<p>REV. DESCRIPTION</p>		<p>1 220 4</p>	
<p>DATE</p>		<p>DESIGNER</p>		<p>APPROVED</p>		<p>DATE</p>		<p>DATE</p>		<p>DATE</p>	
<p>5-10-66</p>		<p>D</p>		<p>A</p>		<p>131820</p>		<p>131820</p>		<p>131820</p>	
<p>5-10-66</p>		<p>D</p>		<p>A</p>		<p>131820</p>		<p>131820</p>		<p>131820</p>	

EOI
TEST POINTS



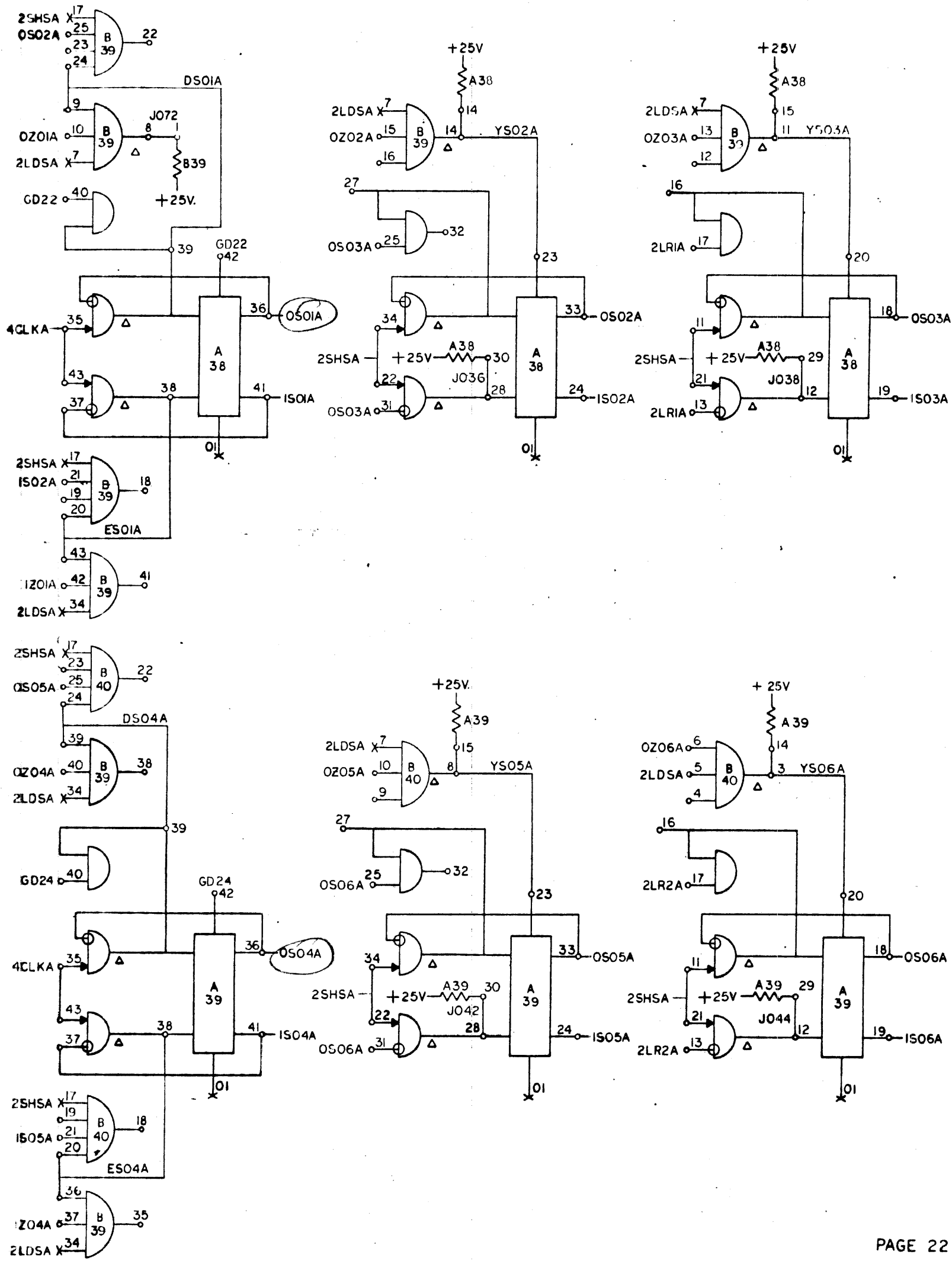
TEST POINTS

D26

- 1 OA09A
- 2 OA10A
- 3 OA11A
- 4 OA12A
- 5 OA13A
- 6 OA14A
- 7 OA15A
- 8 OA16A
- 9 OA17A
- 10 OA18A
- 11 OA19A
- 12 OA20A
- 13 OA21A
- 14 OA22A
- 15 OA23A
- 16 OX03A
- 17 OF01A
- 18 OF02A
- 19 OV01A
- 20 OV02A
- 21 OV03A
- 22 OV04A
- 23 OV05A
- 24 OV06A
- 25 OV07A
- 26 OV08A
- 27 OV09A
- 28 OV10A
- 29 OV11A
- 30 OV12A
- 31 OP06A
- 32 OE01A

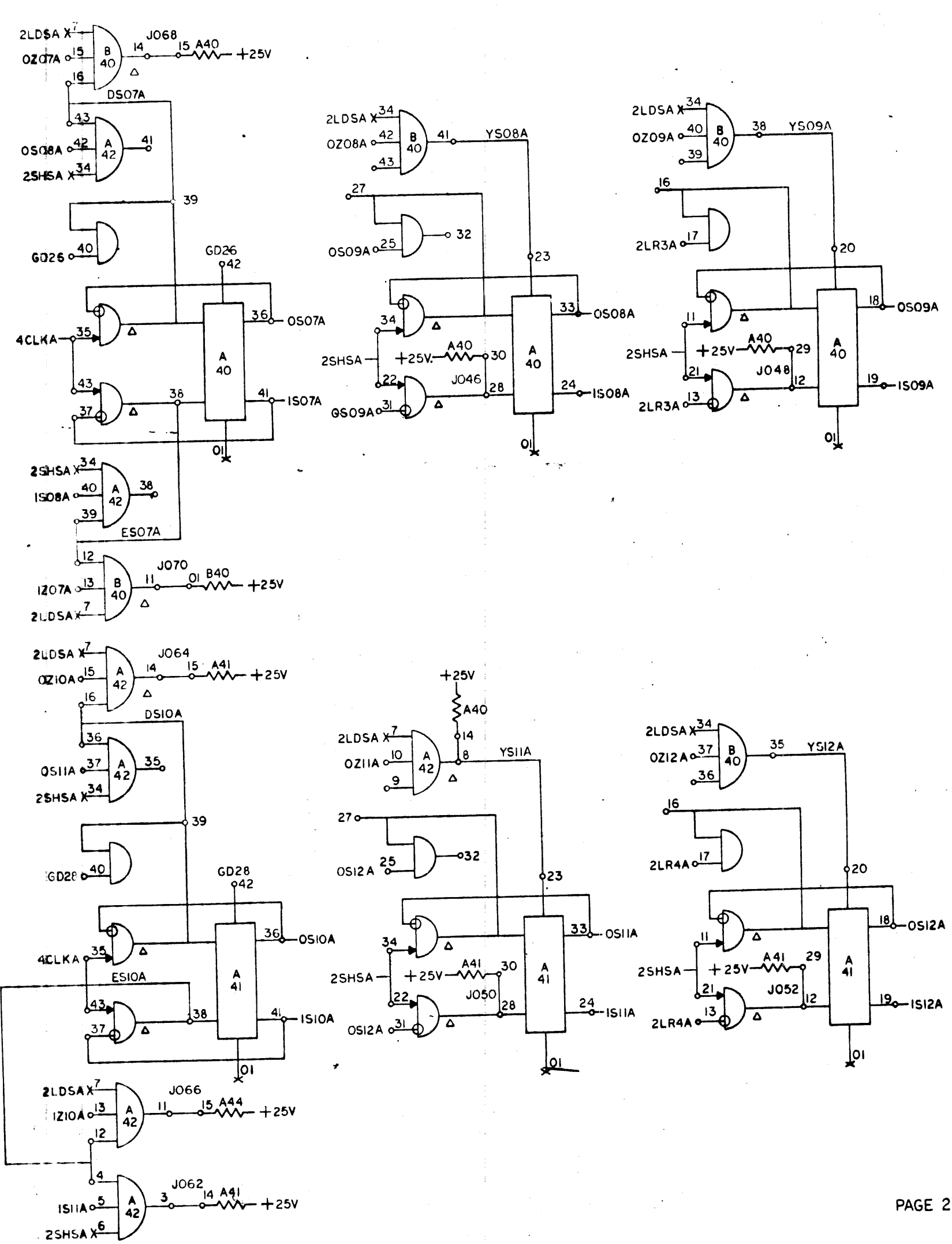
<p>REV DESCRIPTION</p> <p>A MFG RELEASE</p>		<p>REV DATE</p> <p>3 30 66</p>	
<p>DATE 8-17-66</p> <p>BY D</p>		<p>3 320</p> <p>A</p>	
<p>SDS</p> <p>SCIENTIFIC DATA SYSTEMS</p> <p>DIAGRAM, LOGIC, CONTROLLER</p>			

SHIFT REGISTER
S01 → S06



131820 A	REVISIONS NO. DESCRIPTION A MFG RELEASE	131820 A
	DATE	DATE
	APPROVED BY	APPROVED BY
DIAGRAM, LOGIC, CONTROLLER		
DO NOT SCALE DRAWING		
SHEET 22 OF 35		

SHIFT REGISTER
S07 → S12

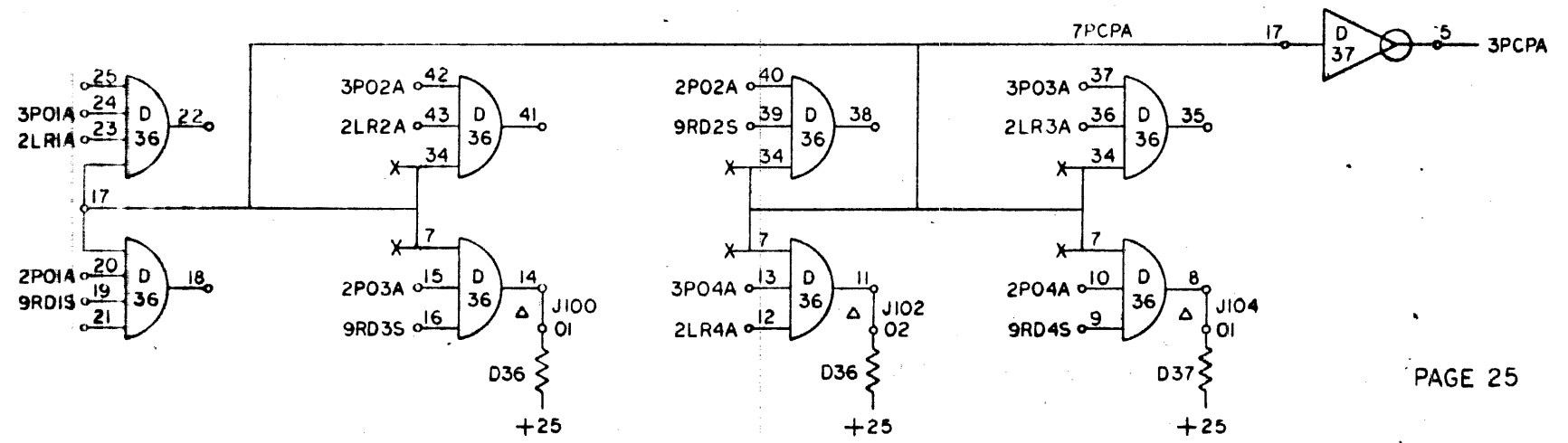
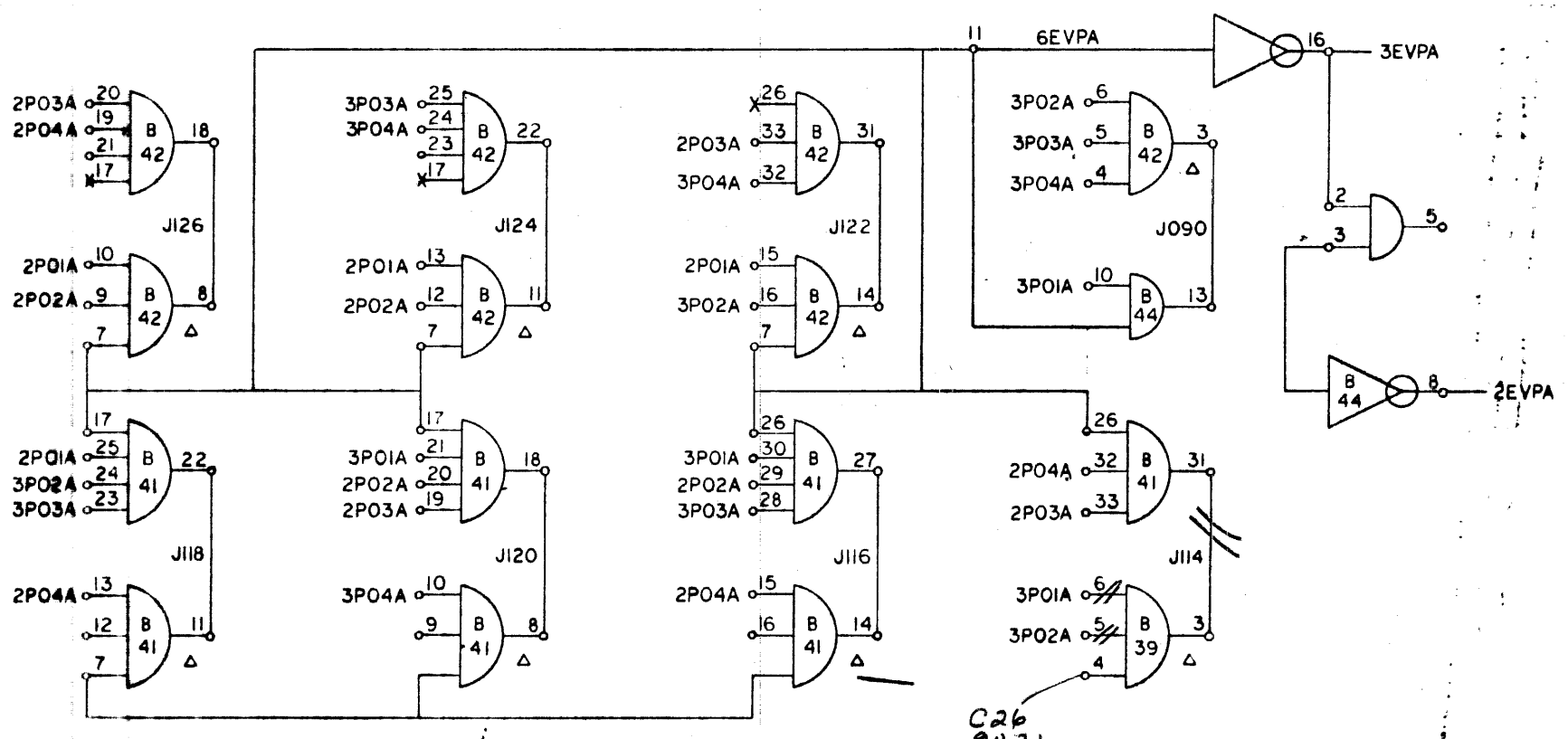
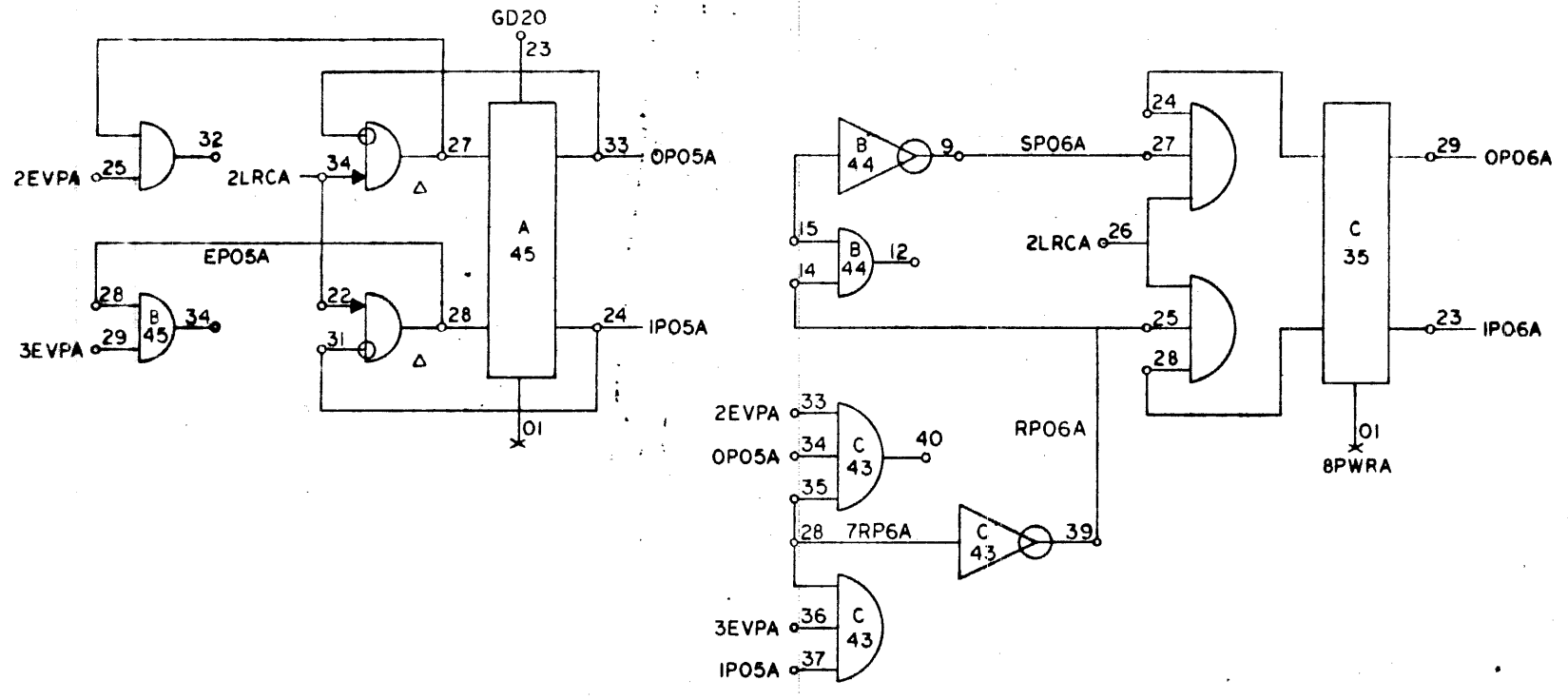


DIAGRAM, LOGIC,
CONTROLLER

131820

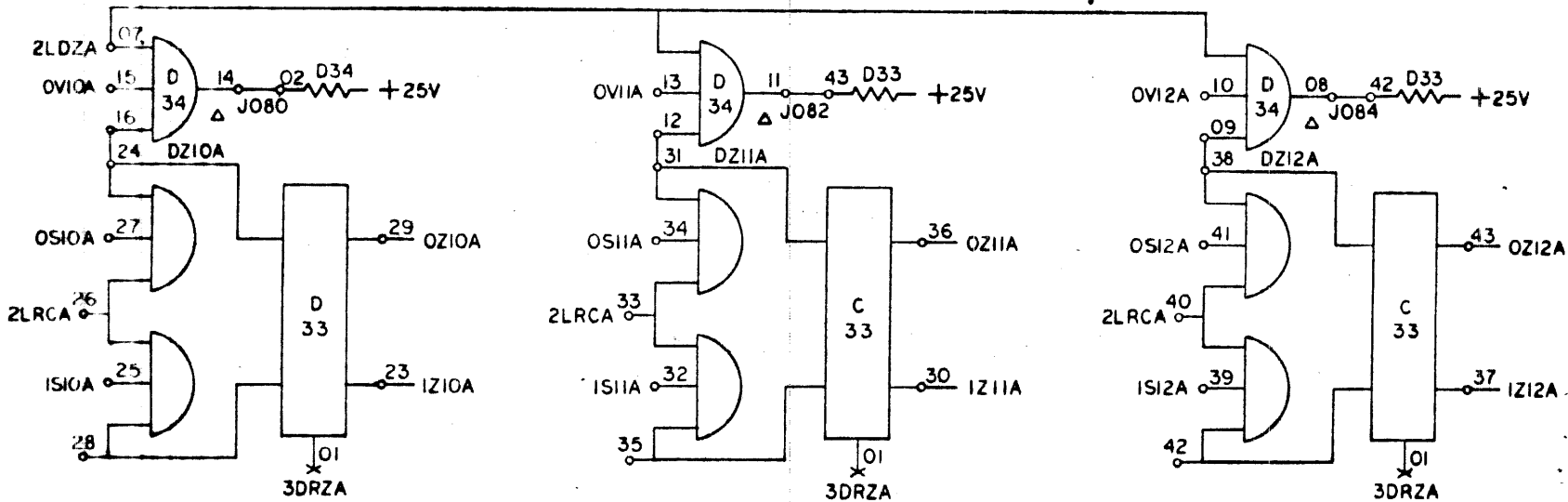
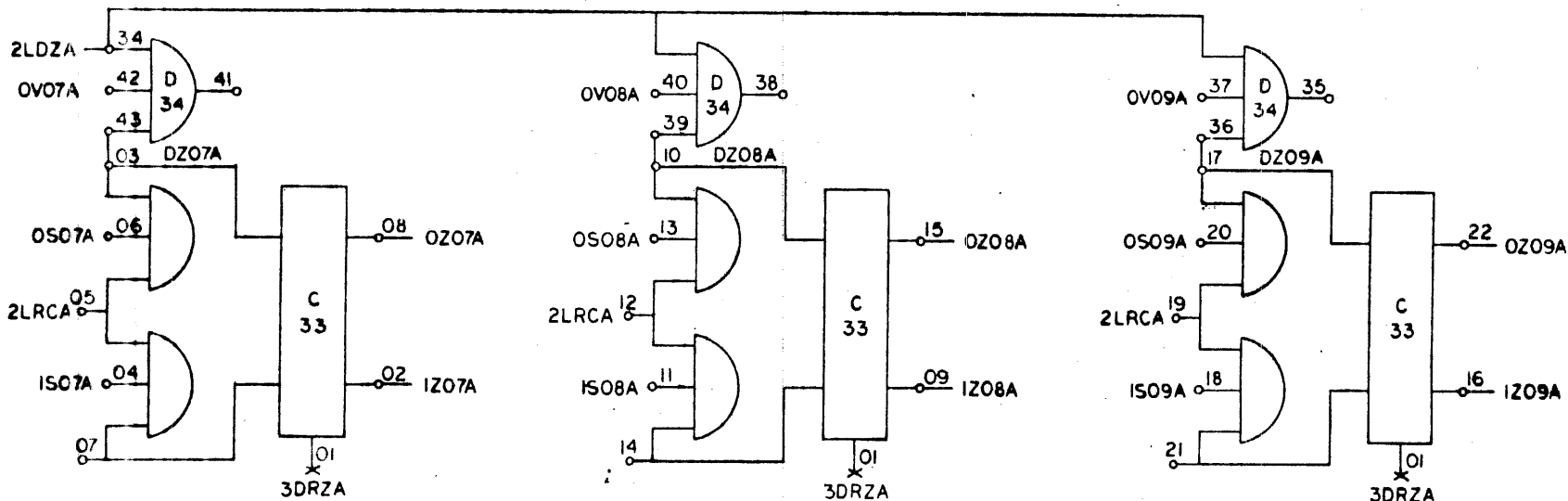
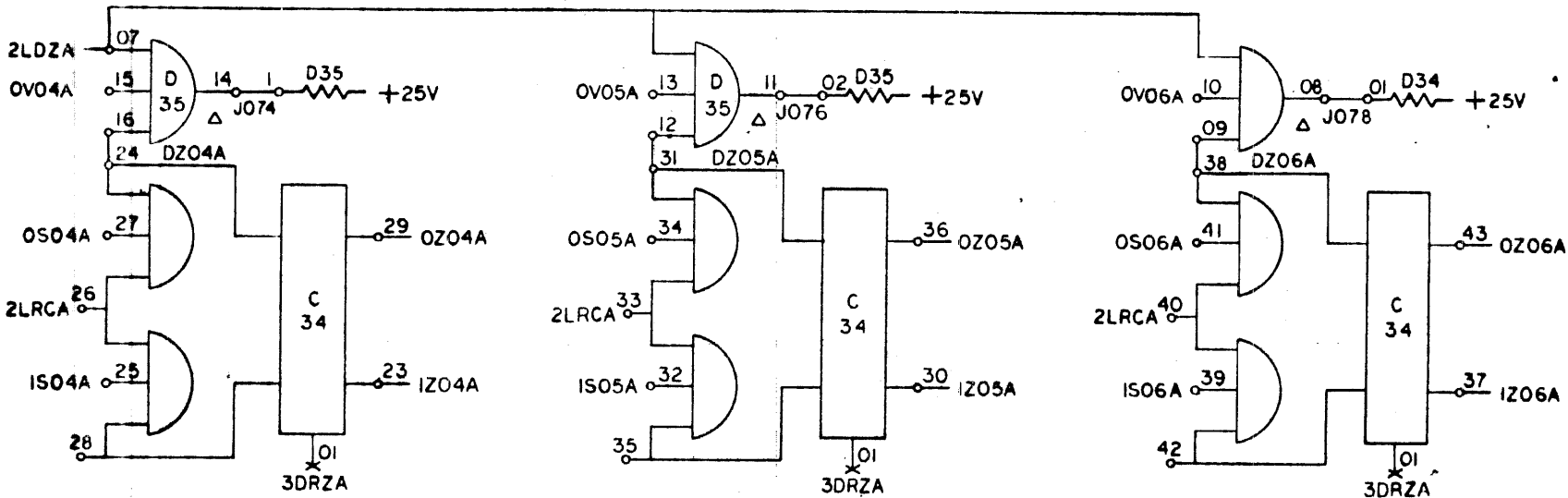
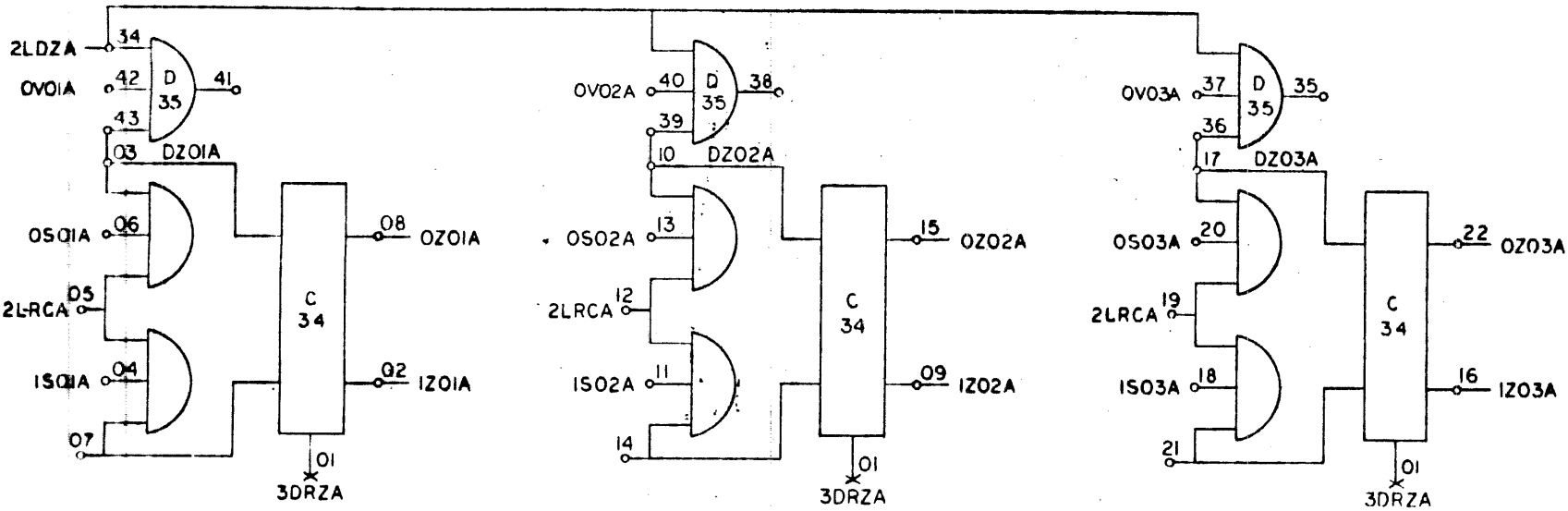
REV	DESCRIPTION	DATE	APPROVED
A	MFG RELEASE		
REVISIONS			
		131820	
		14	

PARITY
P05, P06
EVP, PCP



SCALE	REV	DESCRIPTION	DATE	APPROVED
DO NOT SCALE DRAWING	D	131920		
PAGE 26 OF 35	DIAGRAM, LOGIC, CONTROLLER			
	REVISIONS			
	A	WFS	RELEASE	

Z REGISTER



31820 A	DIAGRAM, LOGIC, CONTROLLER	REVISIONS	
		REV. DESCRIPTION A MFG RELEASE	DATE APPROVED

V REGISTER

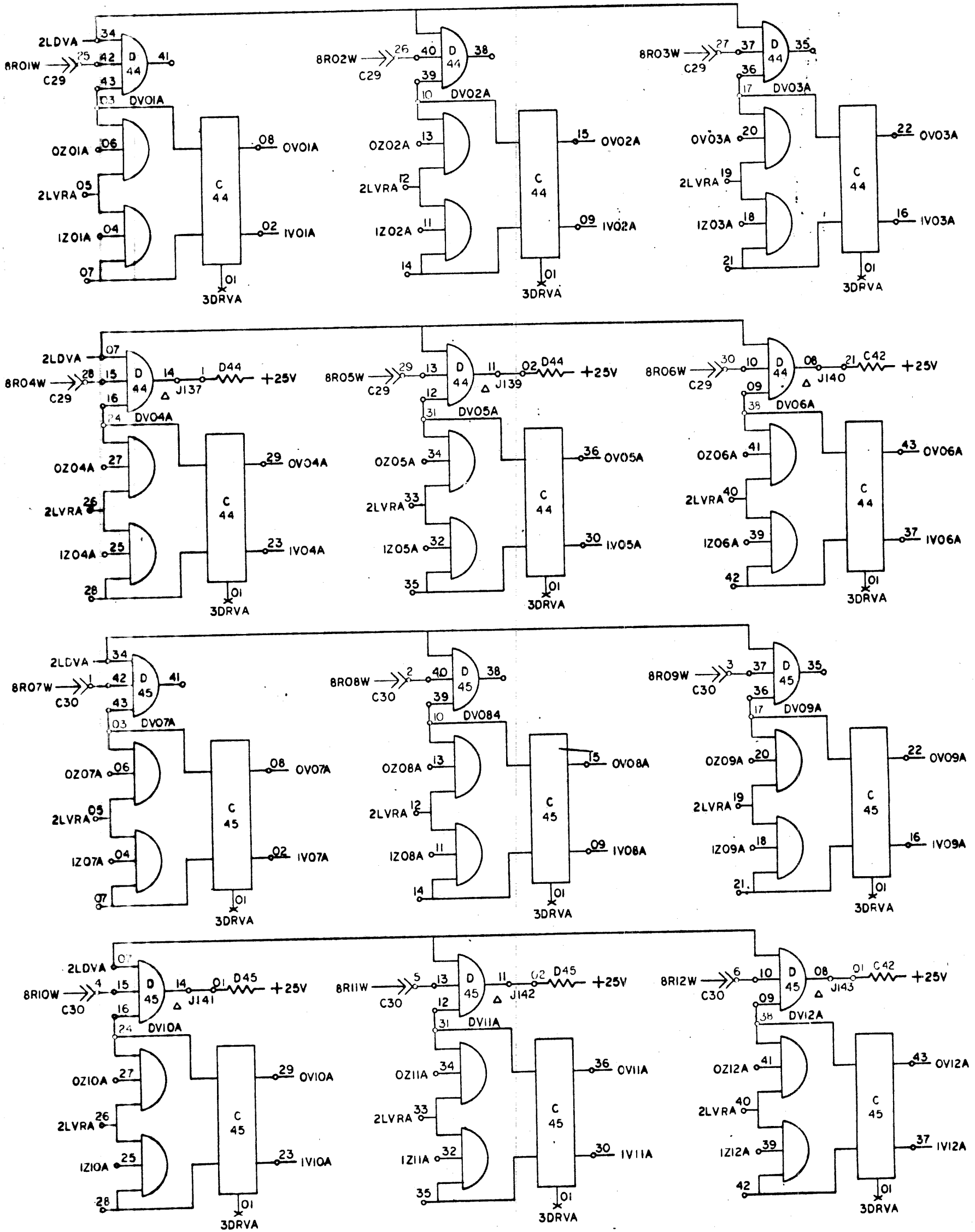


DIAGRAM LOGIC,

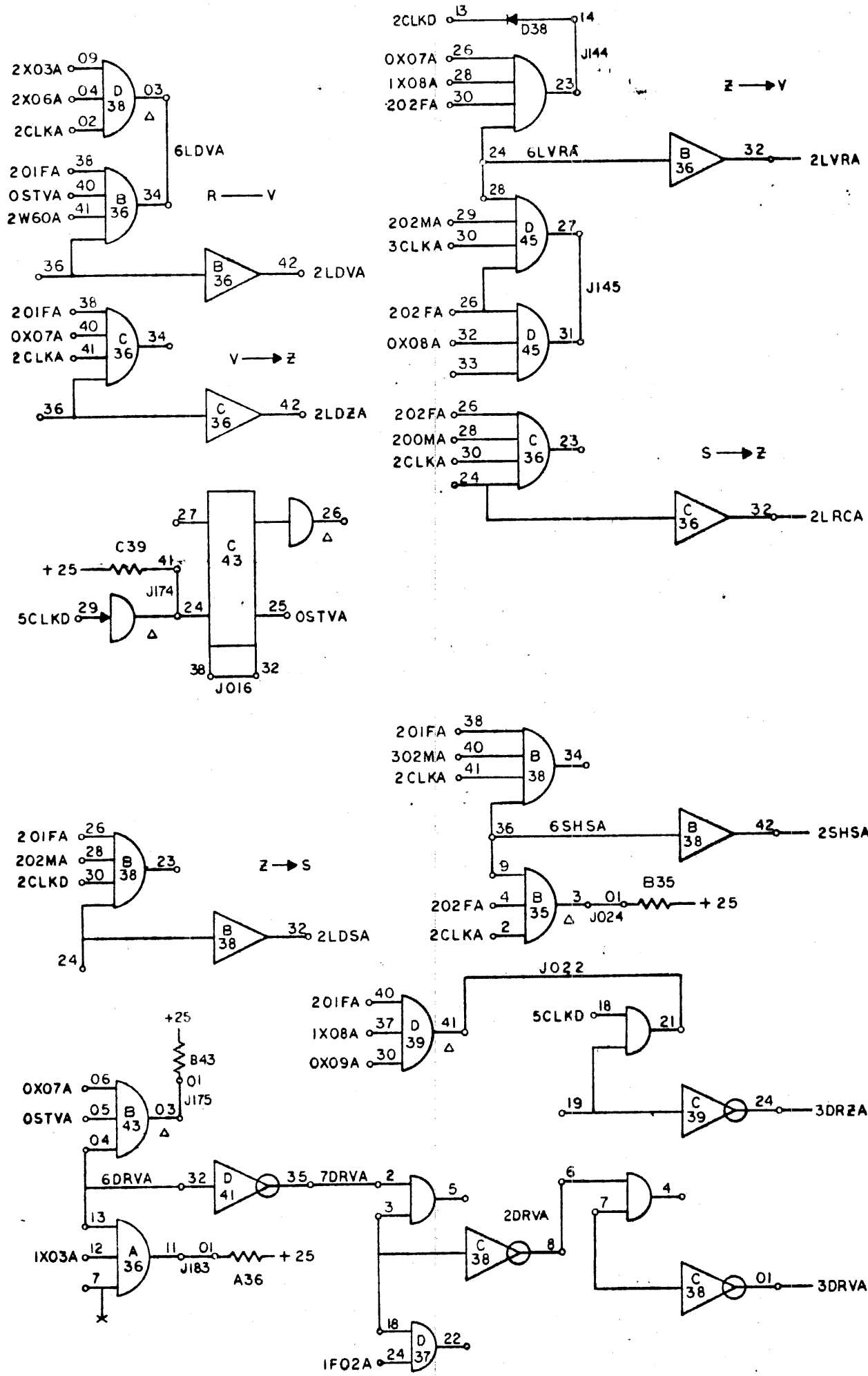
 CONTROLLER

 131820

 A

REVISIONS	
REV.	DESCRIPTION
A	MFG. RELEASE
1	131820
1	A

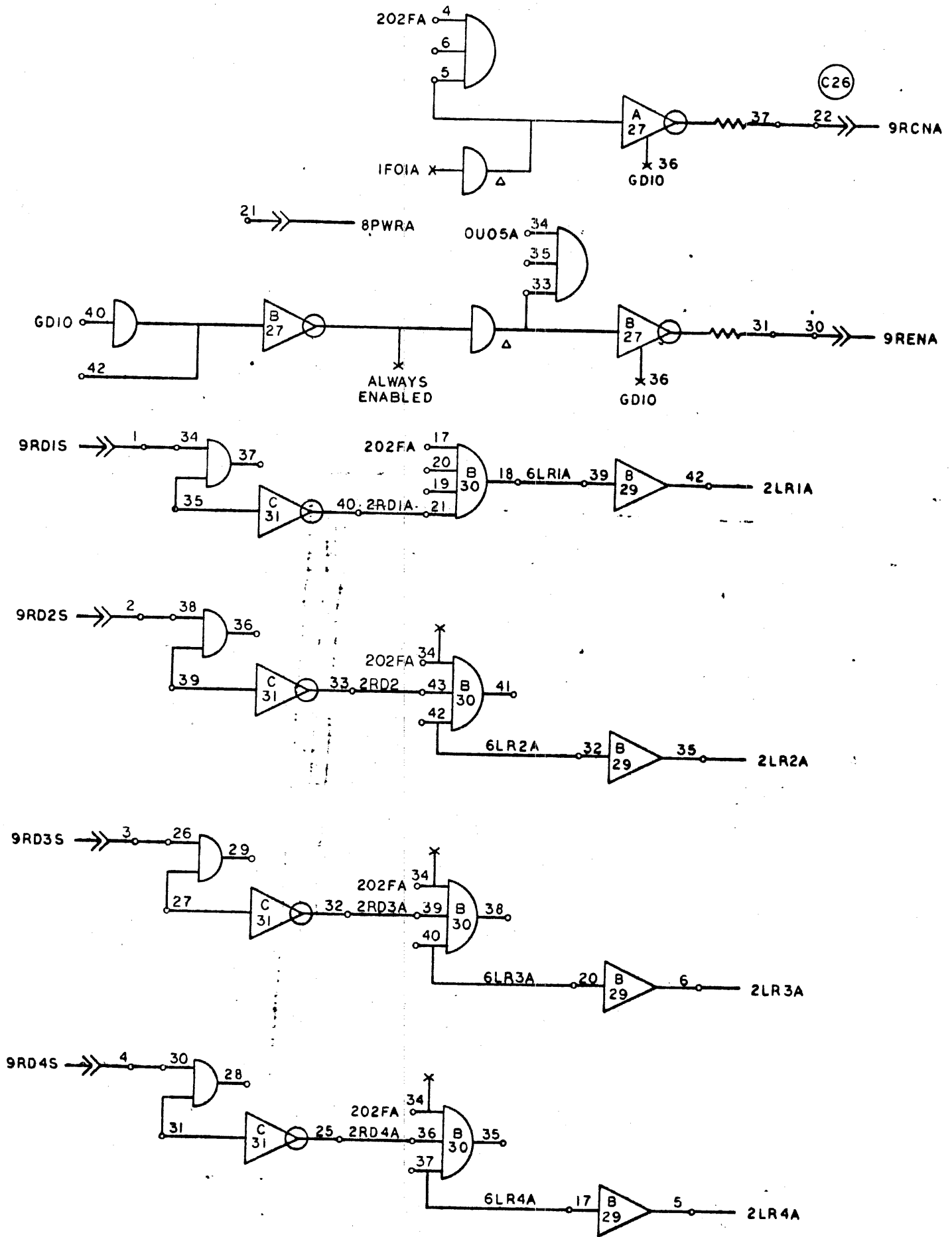
V, Z, S REGISTER LOAD AND SHIFT SIGNALS
LDV, LDZ, LDS, LVR, LRC, SHS, DRV, DRZ, STV



SDB	
DIAGRAM, LOGIC, CONTROLLER	
SIZE	131820
DO NOT SCALE DRAWING	SHEET 28 OF 48

REV	DESCRIPTION	CHK	DATE	APPROVED
A	MFG RELEASE			
REVISIONS				3 200 A

CONTROLLER / SELECTION UNIT
 READ - DATA, CLOCKS, ENABLES
 RDI → 4, LRI → 4: RCN, REN



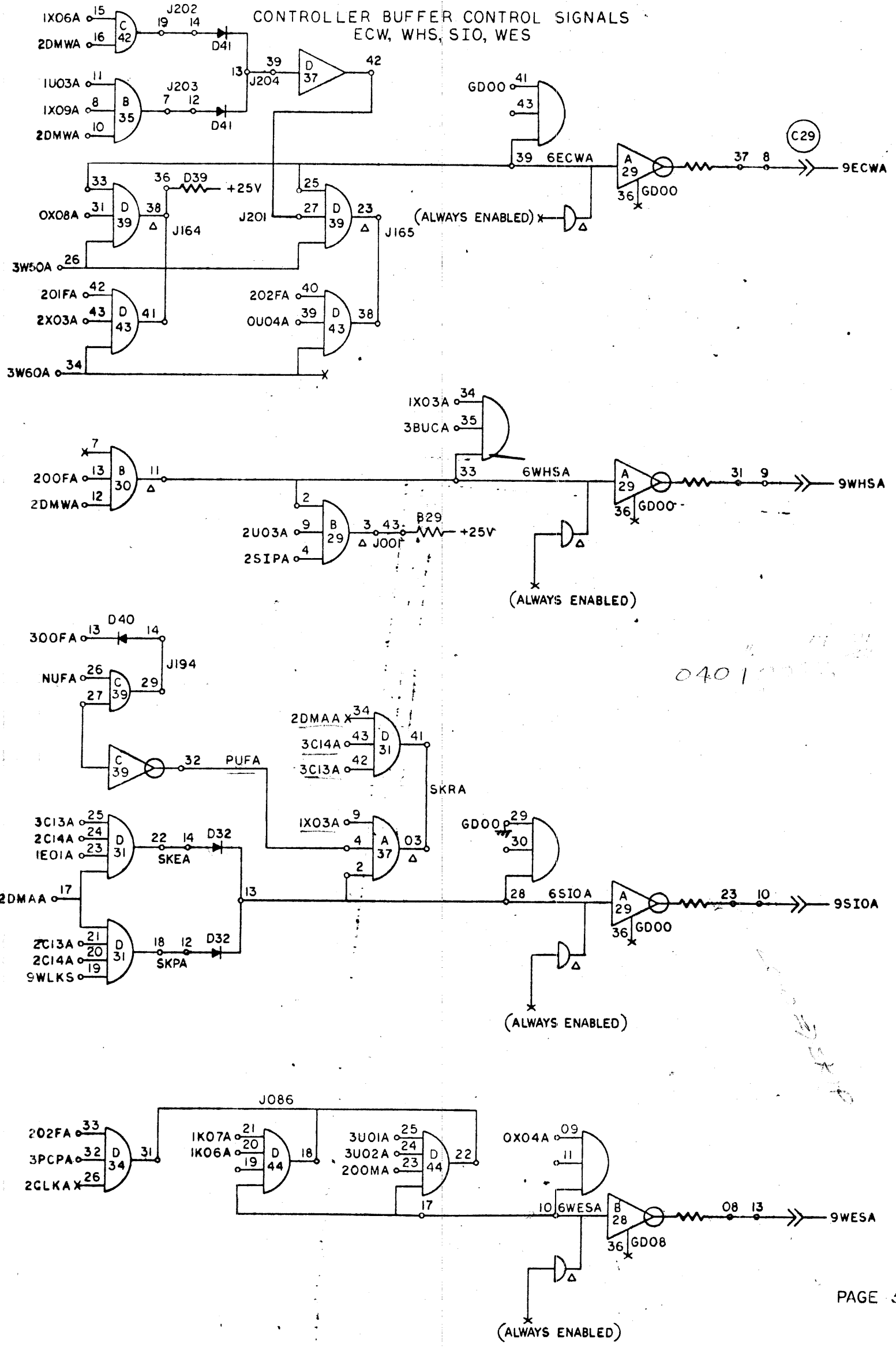
SDS SCIENTIFIC DATA SYSTEMS
 SANTA MONICA, CALIFORNIA

DIAGRAM, LOGIC,
 CONTROLLER

131820

DO NOT SCALE DRAWING SHEET 30 OF 31

REVISIONS	
REV	DESCRIPTION
A	VFG RELEASE



11076
11 17

REV	DESCRIPTION	DATE	BY
A	MFG RELEASE		

DIAGRAM, LOGIC,
CONTROLLER

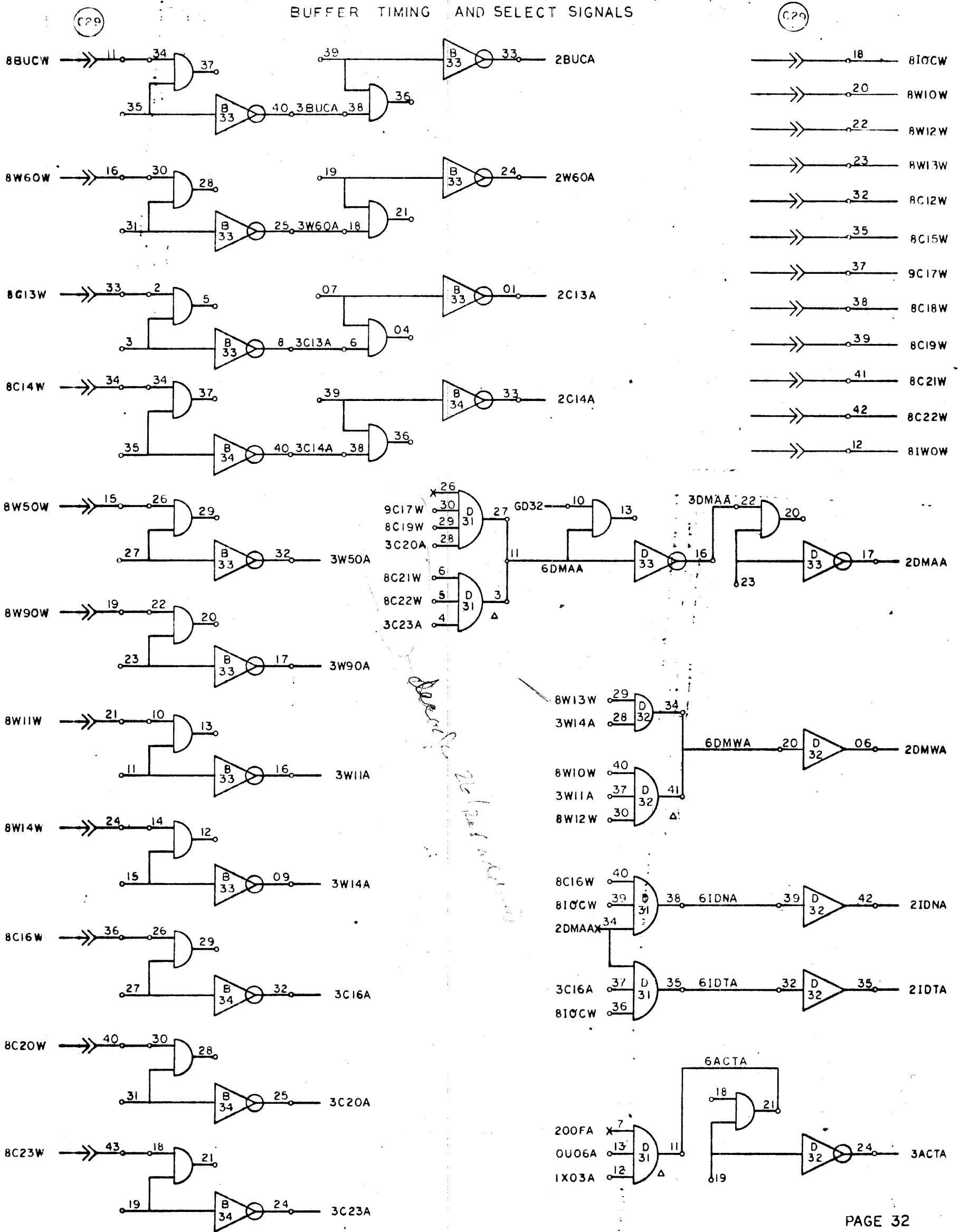
131820

3 OF 35

REV	DESCRIPTION	DATE	BY
A	MFG RELEASE		

3-20-66

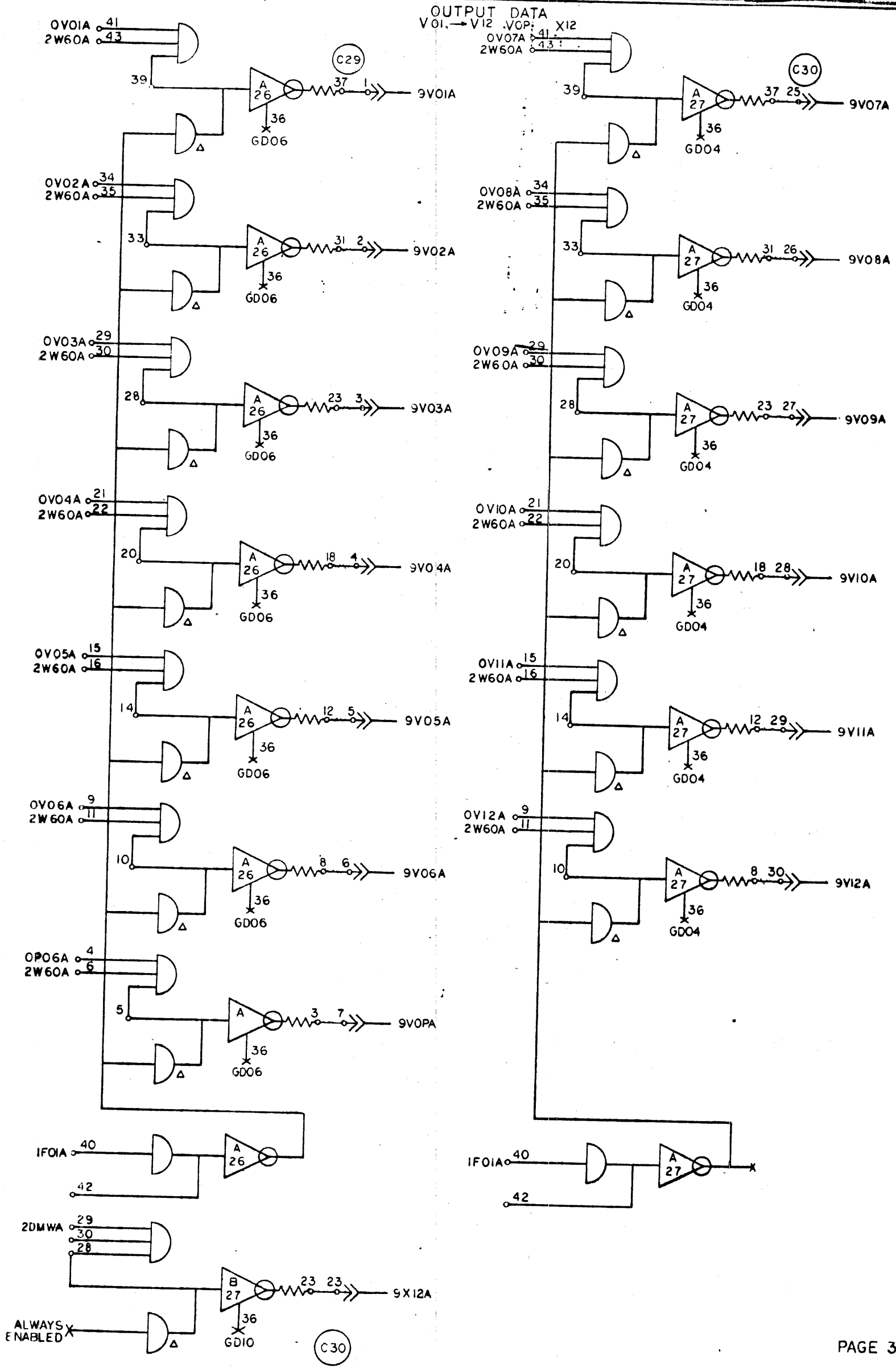
BUFFER TIMING AND SELECT SIGNALS



91011 121314
10/10

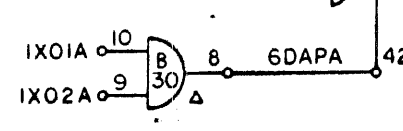
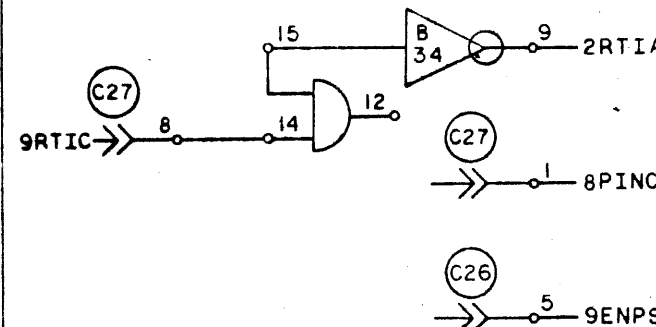
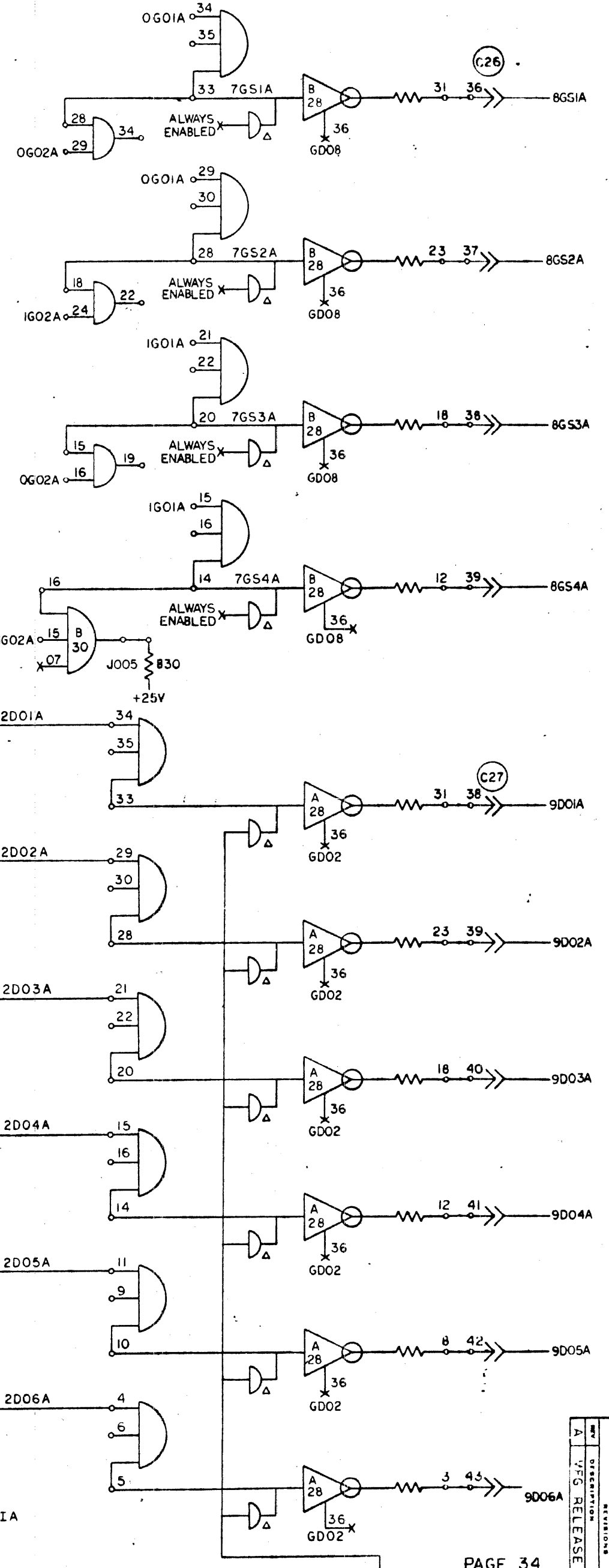
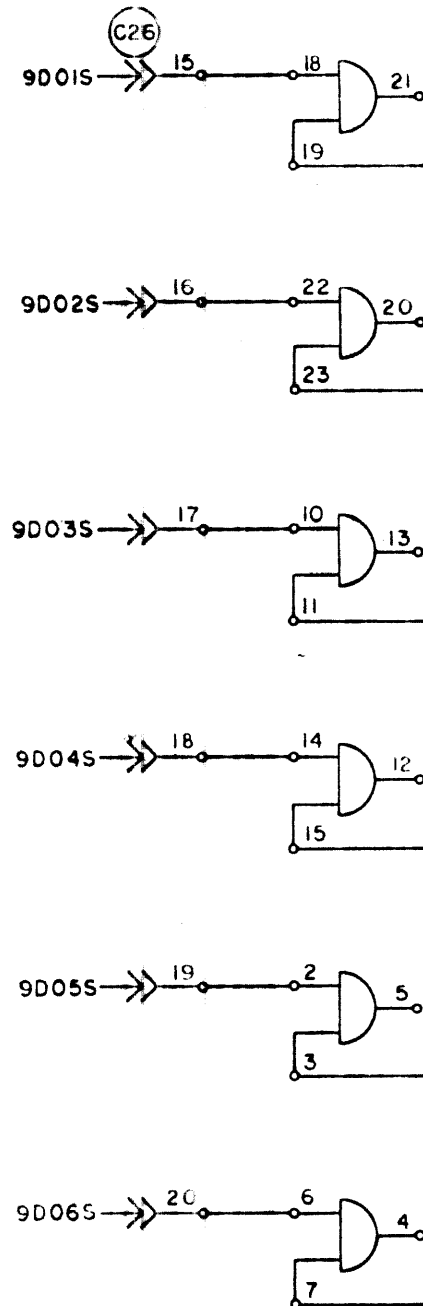
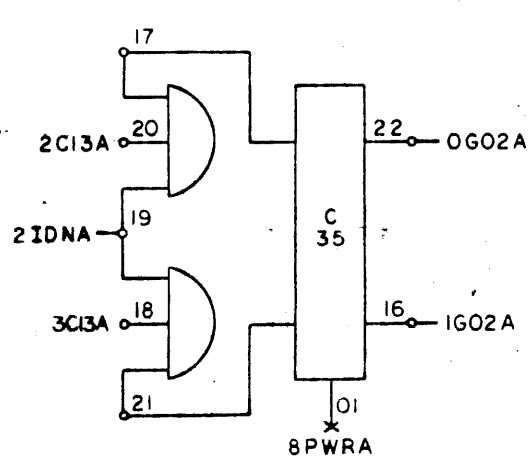
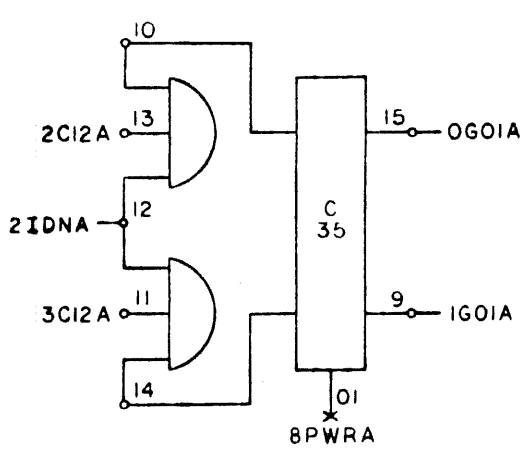
TITLE: **DIAGRAM, LOGIC CONTROLLER**
 PART NO: **131820**
 SHEET: **32** OF **35**

REV	DESCRIPTION	DATE	APPROVED
A	MFG RELEASE		



		REVISIONS	
DIAGRAM, LOGIC, CONTROLLER		REV. 1	DATE 3/8/20
131820		131820	
DO NOT KILL DRAWING SHEET 33 OF 34		131820	

PIN



DATE	131820
REV	D
DESCRIPTION	DIAGRAM, LOGIC, CONTROLLER
APPROVED	
DESIGNED BY	
CHECKED BY	
DATE	8-9-66

REV	DESCRIPTION	DATE	APPROVED
A	VFG RELEASE		